

General Commands Reference Guide L

Release 09.2024

MANUAL

General Commands Reference Guide L

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History

22-Apr-2024 New option **/NoExec** for [LA.IMPORT.TARMAC](#) command.

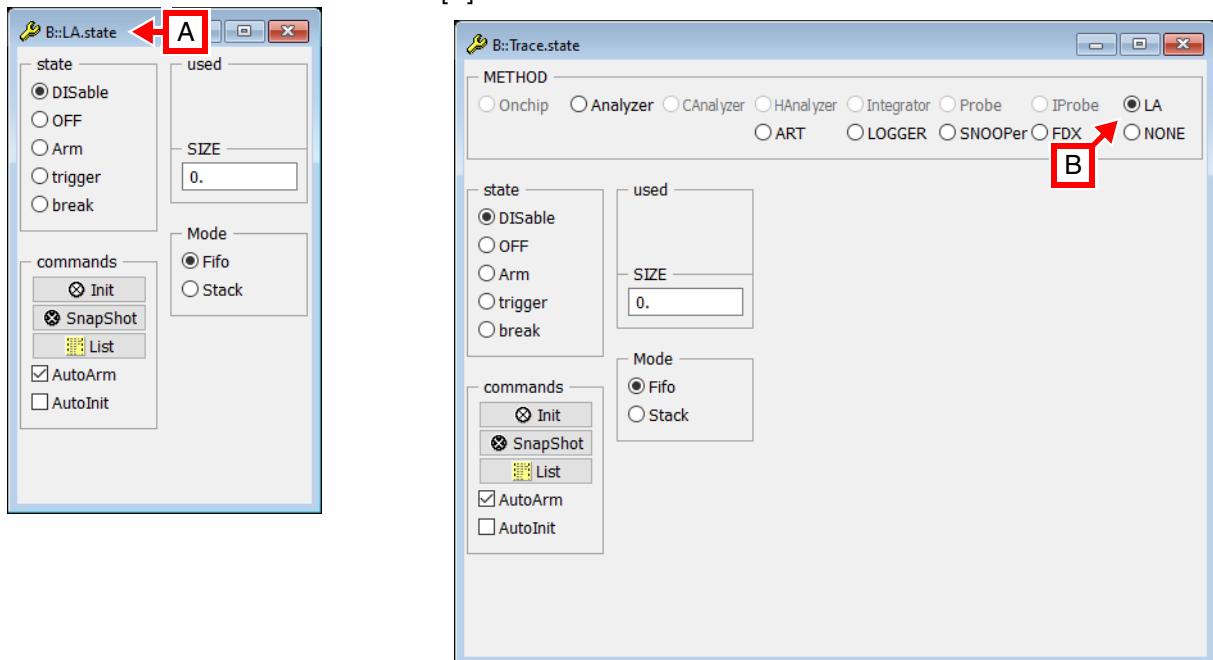
25-Mar-2024 New option **/COVerableItems** for [List.EXPORT.auto](#) and [List.EXPORT.Mix](#) commands.

23-Feb-2024 New command [LA.IMPORT.TARMAC](#).

The trace method LA is used for remote control of logic analyzer systems.

For selecting and configuring the trace method LA, use the TRACE32 command line or a PRACTICE script (*.cmm) or the **LA.state** window [A].

Alternatively, use the **Trace.state** window: click the option **LA** or execute the command **Trace.METHOD LA** [B].



The chapter “[LA-specific Trace Commands](#)”, page 8 describes the LA-specific configuration commands. While the chapter “[Generic LA Trace Commands](#)”, page 20 lists the LA trace analysis and display commands, which are shared with other TRACE32 trace methods.

See also

- [Trace.METHOD](#)
- ▲ ‘[Generic LA Trace Commands](#)’ in ‘[General Commands Reference Guide L](#)’
- ▲ ‘[Release Information](#)’ in ‘[Legacy Release History](#)’

LA.IMPORT

Import trace information

[Example]

The **LA.IMPORT** command group is used to load trace data from a file into TRACE32 and to analyze it just like data recorded with a TRACE32 trace tool.

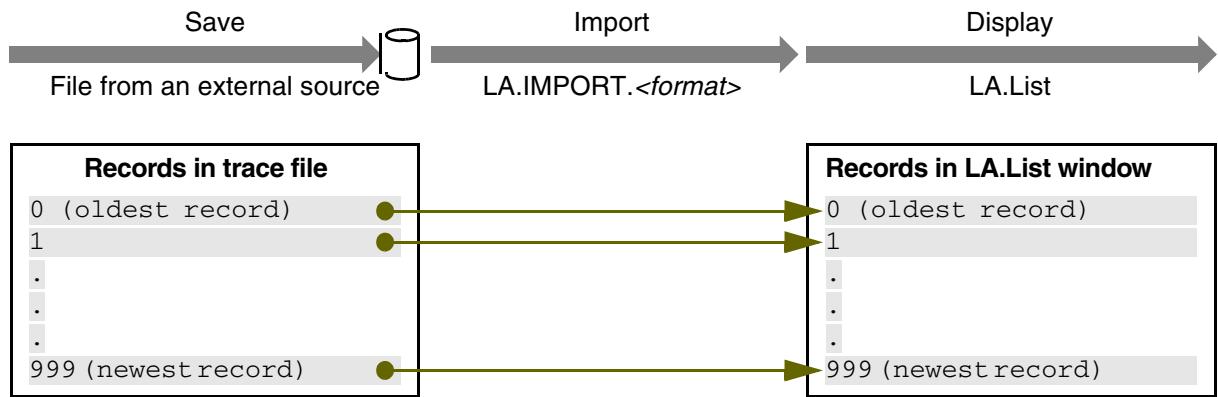
The trace data can be obtained by the application software itself or by another tool or by TRACE32 in a previous debug session in which the processing could not be performed for some reasons.

Trace data successfully obtained and analyzed by TRACE32 can be stored by **<trace>.SAVE** and re-reviewed by using the **<trace>.LOAD** command. This is the more convenient way because **<trace>.SAVE** stores a lot of additional information used for the analysis. **LA.IMPORT** imports only the trace raw data. For proper processing you need to inform the debugger about all the trace-relevant circumstances.

All kind of trace postprocessing is only possible with the trace method **LA** (Logic Analyzer). Therefore you need to use **LA.IMPORT** and **LA.*** command group for all analysis commands or better switch the trace method to **LA** (**Trace.METHOD LA**) and use the command group **Trace.*** for all further operations.

LA.IMPORT supports different kinds of trace data and formats. Therefore different commands are provided.

Most trace data is stored in the file in the timely order the data had been generated.

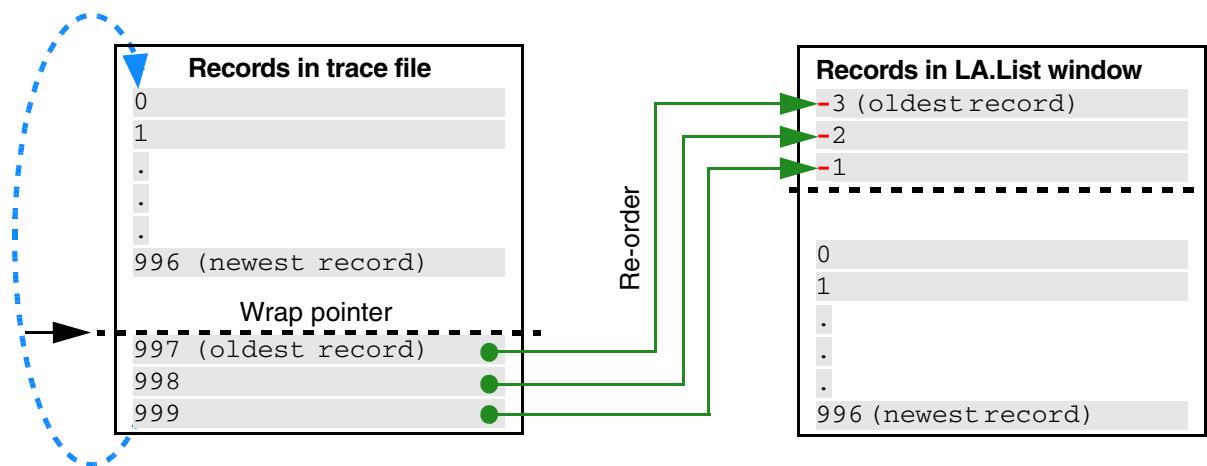


An exception is the on-chip trace buffer, which is typically used as a circular ring buffer overwriting the trace data all the time until the point of interest is reached.

If this buffer is saved into a file, you need to know the wrap pointer for being able to get the data in a timely order. **LA.IMPORT.WRAP** and **LA.IMPORT.GUESSWRAP** will deal with this concern.



Result:



For post processing trace data loaded by **LA.IMPORT** you need to take the following steps:

1. Start TRACE32 to run as simulator (config.t32 -> PBI=SIM). You neither need a debugger hardware nor a target. You can run TRACE32 as debugger as well, but for the postprocessing this is not needed.
2. Adjust all trace relevant settings like for a real target by running the start-up script you used for generating the trace data. For postprocessing an ETMv4 even further setups might be needed which normally the debugger would read out from the ETM module (ETM.COND, ETM.INSTPO, ETM.QE).

If the start-up script is **not** available, then try this:

- At best selecting the chip you are debugging (**SYStem.CPU ...**) is sufficient.
- For trace data coming from a ARM CoreSight system, all commands describing the trace system on the chip are required (**SYStem.CONFIG ...**).
- Further all settings for the trace sources done at recording time are needed (e.g. ETM.).

3. Load your target application (**Data.LOAD ...**).
4. Import the trace raw data (**LA.IMPORT ...**).
5. Now you can use all trace display and analysis functions, e.g.

```
LA.List TP TPC TPINFO DEFault List.NoDummy.OFF ; with diagnostics
```

Example:

```
SYStem.CPU CortexA15

SYStem.CONFIG COREDEBUG.Base 0x82010000
SYStem.CONFIG.ETM.Base 0x8201c000
SYStem.CONFIG.FUNNEL.Base 0x80040000
SYStem.CONFIG.FUNNEL.ATBSource ETM 0
SYStem.CONFIG.ETB.Base 0x80010000

ETM.PortMode.Wrapped
ETM.TraceID 0x55

SYStem.Up

Data.LOAD.Elf myfile.elf

Trace.METHOD.LA
Trace.IMPORT.ETB mydata.bin
Trace.IMPORT.GUESSWRAP

Trace.List TP TPC TPINFO DEFault List.NoDummy.OFF
```

See also

- [LA.IMPORT.CoreByteStream](#)
- [LA.IMPORT.ELA](#)
- [LA.IMPORT.GUESSWRAP](#)
- [LA.IMPORT.StartValid](#)
- [LA.IMPORT.STPByteStream](#)
- [LA.IMPORT.TraceFile](#)
- [LA.IMPORT.UltraSOC](#)
- [LA.IMPORT.WRAP](#)
- [LA.IMPORT.cycles](#)
- [LA.IMPORT.ETB](#)
- [LA.IMPORT.StartInvalid](#)
- [LA.IMPORT.STP](#)
- [LA.IMPORT.TARMAC](#)
- [LA.IMPORT.TracePort](#)
- [LA.IMPORT.VCD](#)
- [<trace>.EXPORT](#)

LA.IMPORT.CoreByteStream

Import pure single core trace data

Format: **LA.IMPORT.CoreByteStream <file>**

Imports pure single core trace data (e.g. for unwrapped single core trace data or x86 IPT traces).

See also

- [LA.IMPORT](#)

Format: **LA.IMPORT.cycles <file>**

Re-imports a file that has been exported with [**<trace>.EXPORT.cycles**](#). This bus trace data comes from capturing the fetched instructions and data accesses done on an external bus to figure out the program behavior. It works only if no cache is used and if the bus accesses can be captured. This command can be used to import traces from external tools or simulators.

See also

- [LA.IMPORT](#)
- ▲ 'Release Information' in 'Legacy Release History'

LA.IMPORT.ELAImport ELA trace data

Format: **LA.IMPORT.ELA <file>**

Imports a pure binary trace data file obtained from an ARM CoreSight Embedded Logic Analyzer ([ELA](#)).

See also

- [LA.IMPORT](#)

LA.IMPORT.ETBImport on-chip trace data

Format: **LA.IMPORT.ETB <file>**

Imports a pure binary trace data file obtained from an on-chip trace buffer like ARM CoreSight ETB, ETF, ETR.

You additionally need to use [LA.IMPORT.WRAP](#) or [LA.IMPORT.GUESSWRAP](#) if the following conditions apply:

- The on-chip trace buffer was used as a circular ring buffer.
- The on-chip trace data was stored as is, it was not read out in the timely order starting from the write pointer position.

LA.Mode FlowTrace will automatically be set when using this command.

See also

- [LA.IMPORT](#)
- ▲ 'Release Information' in 'Legacy Release History'

Format: **LA.IMPORT.GUESSWRAP** [*<record_number>*]

Reformats external trace data loaded to TRACE32 in a timely order. The external trace data of a circular ring buffer is loaded to TRACE32 using **LA.IMPORT.ETB**. The command **LA.IMPORT.GUESSWRAP** scans the loaded trace data and guesses where the wrap pointer might have been.

Optionally, you can pass a record number where the search for the wrap pointer shall start. Without a parameter it starts from the beginning.

Use **LA.IMPORT.WRAP** if you know where the wrap pointer is.

See the figures in the introduction to **<trace>.IMPORT**.

See also

- [LA.IMPORT](#)
- [LA.IMPORT.WRAP](#)

LA.IMPORT.StartInvalid

Set start of trace as invalid

Format: **LA.IMPORT.StartInvalid**

Reverts the setting done with **LA.IMPORT.StartValid**.

See also

- [LA.IMPORT](#)

Format: **LA.IMPORT.StartValid** [*<address1> <address2> ...*]

This command informs the debugger that the start of the loaded trace is valid and that it should not wait for Sync packets.

<address1> The trace is set as valid starting from the given address. On SMP systems, the first address corresponds to the first core, the second to the second core...

<address2> ...

See also

■ [LA.IMPORT](#)

LA.IMPORT.STP**Import STP recording from file (nibble)**

Format: **LA.IMPORT.STP** *<file>*

Imports an STP trace from *<file>* to process it within TRACE32. One trace record is generated per nibble.

In order to unwrap the trace information for processing, TRACE32 needs to know the following information: STM base address and the STP protocol version.

If TRACE32 is aware of the chip characteristic, setting up the chip is sufficient.

Example:

```
SYStem.CPU OMAP4430APP1
LA.IMPORT.STP my_recording.stp
STMLA.List
```

Otherwise the following setup has to be done.

```
SYStem.CONFIG.STM.Base DAP:0xd4161000 ; any base address != 0x0 is  
; fine  
SYStem.CONFIG.STM.Mode STPv2 ; specify the STP protocol  
; version  
LA.IMPORT.STP my_recording.stp  
STMLA.List
```

See also

- [LA.IMPORT](#)

LA.IMPORT.STPByteStream

Import STP recording from file (byte)

Format: **LA.IMPORT.STPByteStream <file>**

Same as [LA.IMPORT.STP](#), but one trace record is generated per byte.

See also

- [LA.IMPORT](#)
- ▲ 'Release Information' in 'Legacy Release History'

LA.IMPORT.TARMAC

Import TARMAC trace file

[build 165454 - DVD 02/2024]

Format: **LA.IMPORT.TARMAC <file> [/<option>]**

<option>: **NoExec**

Imports a trace data file in Tarmac format.

| | |
|---------------|---|
| NoExec | Allows the import process to ignore the execution state information (exec/notexec) from the file. |
|---------------|---|

See also

- [LA.IMPORT](#)

Format: **LA.IMPORT.TraceFile <file>**

Re-imports trace data stored by [**<trace>.SAVE**](#) for re-processing. This is useful if processing was not possible when the trace recording was made. For example if you had no access to the target code at that moment.

Only the trace raw data will be extracted from the saved (*.ad) file.

[**LA.Mode FlowTrace**](#) will automatically be set when using this command.

See also

- [LA.IMPORT](#)
- ▲ 'Release Information' in 'Legacy Release History'

Format: **LA.IMPORT.TracePort <file>**

Imports a pure binary trace data file from an external trace port like an ARM CoreSight TPIU. Unlike on-chip trace data, off-chip trace data includes synchronization packages and depend on the port size of the trace port.

[**LA.Mode FlowTrace**](#) will automatically be set when using this command.

See also

- [LA.IMPORT](#)
- ▲ 'Release Information' in 'Legacy Release History'

Format: **LA.IMPORT.UltraSOC <file>**

This command allows to load raw UltraSOC flow trace data.

See also

- [LA.IMPORT](#)

LA.IMPORT.VCD

Import recorded signals in VCD file format

Format: **LA.IMPORT.VCD <file>**

Imports a VCD (Value Change Dump) file, which is an industrial standard format for waveforms (not for program trace). It is used for visualizing and analyzing the captured signals in the [**<trace>.Timing**](#) window.

See also

- [LA.IMPORT](#)

Format: **LA.IMPORT.WRAP <record_number>**

Reformats external trace data loaded to TRACE32 in a timely order. The external trace data of a circular ring buffer is loaded to TRACE32 using [LA.IMPORT.ETB](#).

<*record_number*> You pass the <*record_number*> of the first trace record in time (wrap pointer). This is the write pointer location of a circular ring buffer the moment the data has been stored.

NOTE: On a CoreSight trace, the write pointer points to a 32-bit value. You need to multiply this value by 4 because each CoreSight trace record is 8 bit in size.

Use [LA.IMPORT.GUESSWRAP](#) if you do not know where the wrap pointer is.

See the figures in the introduction to [LA.IMPORT](#).

See also

- [LA.IMPORT](#)
- [LA.IMPORT.GUESSWRAP](#)

Format: **LA.Mode** [*<mode>*]

<mode>: **Fifo**
 Stack
 FlowTrace

Selects the trace operation mode.

- | | |
|------------------|--|
| Fifo | If the trace is full, new records will overwrite older records. The trace records always the last cycles before the break. |
| Stack | If the trace is full recording will be stopped. The trace always records the first cycles after starting the trace. |
| FlowTrace | FlowTrace mode. |

See also

- [<trace>.Mode](#)

Generic LA Trace Commands

LA.ACCESS Define access path to program code for trace decoding

See command [**<trace>.ACCESS**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 131).

LA.Arm Arm the trace

See command [**<trace>.Arm**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 134).

LA.AutoArm Arm automatically

See command [**<trace>.AutoArm**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 135).

LA.AutoInit Automatic initialization

See command [**<trace>.AutoInit**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 140).

LA.BookMark Set a bookmark in trace listing

See command [**<trace>.BookMark**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 140).

LA.Chart Display trace contents graphically

See command [**<trace>.Chart**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 144).

See command [**<trace>.CLOCK**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 191).

See command [**<trace>.ComPare**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 192).

See command [**<trace>.ComPareCODE**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 194).

See command [**<trace>.DISable**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 197).

See command [**<trace>.DRAW**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 201).

See command [**<trace>.EXPORT**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 212).

See command [**<trace>.FILE**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 233).

See command [**<trace>.Find**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 235).

See command [**<trace>.FindAll**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 237).

LA.FindChange

Search for changes in trace flow

See command [**<trace>.FindChange**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 238).

LA.FLOWPROCESS

Process flowtrace

See command [**<trace>.FLOWPROCESS**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 241).

LA.FLOWSTART

Restart flowtrace processing

See command [**<trace>.FLOWSTART**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 241).

LA.GOTO

Move cursor to specified trace record

See command [**<trace>.GOTO**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 244).

LA.Init

Initialize trace

See command [**<trace>.Init**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 246).

LA.List

List trace contents

See command [**<trace>.List**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 248).

See command [**<trace>.ListNesting**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 263).

LA.ListVar

List variable recorded to trace

See command [**<trace>.ListVar**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 266).

LA.LOAD

Load trace file for offline processing

See command [**<trace>.LOAD**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 270).

LA.OFF

Switch off

See command [**<trace>.OFF**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 278).

LA.PROfileChart

Profile charts

See command [**<trace>.PROfileChart**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 283).

LA.PROfileSTATistic

Statistical analysis in a table versus time

See command [**<trace>.PROfileSTATistic**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 322).

LA.PROTOcol

Protocol analysis

See command [**<trace>.PROTOcol**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 339).

See command [**<trace>.PROTOcol.Chart**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 339).

See command [**<trace>.PROTOcol.Draw**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 341).

See command [**<trace>.PROTOcol.EXPORT**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 342).

See command [**<trace>.PROTOcol.Find**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 343).

See command [**<trace>.PROTOcol.list**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 344).

See command [**<trace>.PROTOcol.PROfileChart**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 347).

See command [**<trace>.PROTOcol.PROfileSTATistic**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 348).

See command [**<trace>.PROTOcol.STATistic**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 350).

LA.REFSet reference point for time measurement

See command [**<trace>.REF**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 357).

LA.RESetReset command

See command [**<trace>.RESet**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 357).

LA.SAVESave trace for postprocessing in TRACE32

See command [**<trace>.SAVE**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 358).

LA.SelfArmAutomatic restart of trace recording

See command [**<trace>.SelfArm**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 362).

LA.SIZEDefine buffer size

See command [**<trace>.SIZE**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 373).

LA.SnapShotRestart trace capturing once

See command [**<trace>.SnapShot**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 373).

LA.stateDisplay trace configuration window

See command [**<trace>.state**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 376).

See command [**<trace>.STATistic**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 378).

LA.Timing

Waveform of trace buffer

See command [**<trace>.Timing**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 499).

LA.TRACK

Set tracking record

See command [**<trace>.TRACK**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 502).

LA.View

Display single record

See command [**<trace>.View**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 504).

LA.ZERO

Align timestamps of trace and timing analyzers

See command [**<trace>.ZERO**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 505).

List

Features program listing display

Program Listing Across Multiple Display Formats

The **List** command group displays a program listing:

- Source code and object code or source code only in the **List.auto** window dynamically determined by the **current debug mode**.
- Source code and object code in the **List.Mix** window
- Object code only in the **List.Asm** window
- Source code only in the **List.Hll** window
- Java byte code in the **List.Java** window

Displayed Program Section

If no address is specified, the window automatically tracks the program counter (PC).

```
List.Auto
```

If an address or symbol is used, the program display begins at that address. The window remains permanently anchored to this address.

```
List.Auto func5
```

Disassembler Format

You can customize the disassembler format using the **SETUP.DIS** command.

Hatched Lines

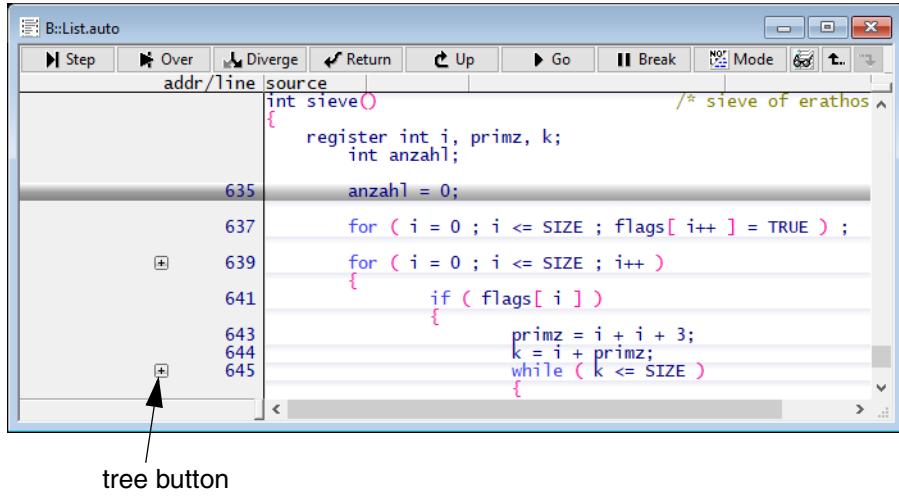
The screenshot shows the TRACE32 debugger interface with the title bar [B::List]. The main window displays assembly code in a table format. Hatched lines are used to highlight specific memory locations or sections of the assembly code.

| addr/line | code | label | mnemonic | comment |
|--------------|----------|-------|-------------------|--------------------|
| MX:2E0000E4 | D10183FF | main: | sub sp,sp,#0x60 | ; sp,sp,#96 |
| 548 | | | | |
| MX:2E0000E8 | 14000076 | b | 0x2E000FC0 | |
| 551 | | | | |
| MX:2E0000ECD | D503201F | | nop | |
| MX:2E0000DF0 | 52800640 | | mov w0,#0x32 | ; return,#50 |
| MX:2E0000DF4 | B9005FE0 | | str w0,[sp,#0x5C] | ; wreturn,[sp,#92] |
| 552 | | | | |
| MX:2E0000DF8 | 52800540 | | mov w0,#0x2A | ; return,#42 |
| MX:2E0000DFC | 2A0003F3 | | mov w19,w0 | ; w19,return |
| 554 | | | | |
| MX:2E0000E00 | 910173E0 | | add x0,sp,#0x5C | ; return,sp,#92 |

The debug information for the executable also includes the paths to the source files. Hatched lines indicate that TRACE32 cannot locate the source files at the specified paths. Use a command from the **sYmbol.SourcePATH** command group to correct the paths to the source files.

Tree Button

If the source listing is displayed in HLL mode, some code lines may be marked with a tree button. This tree button indicates that the compiler generated detached blocks of assembler code at more than one address location for this source code line. This is very common for **for-while** loops as well as for code compiled with a high optimization level.



| B::SymbolList.LINE | | | |
|---------------------|------------------|---|----------|
| address | module | source | line |
| P:2E000D94-2E000D9B | \sieve_a64\sieve | D:\development\trunk\demo\demo\arm64\compiler\arm\sieve.c | 642--643 |
| P:2E000D9C-2E000DA3 | \sieve_a64\sieve | D:\development\trunk\demo\demo\arm64\compiler\arm\sieve.c | 644--644 |
| P:2E000DA4-2E000DA7 | \sieve_a64\sieve | D:\development\trunk\demo\demo\arm64\compiler\arm\sieve.c | 645--645 |
| P:2E000DA8-2E000DB3 | \sieve_a64\sieve | D:\development\trunk\demo\demo\arm64\compiler\arm\sieve.c | 646--647 |
| P:2E000DB4-2E000DBB | \sieve_a64\sieve | D:\development\trunk\demo\demo\arm64\compiler\arm\sieve.c | 648--648 |
| P:2E000DBC-2E000DC3 | \sieve_a64\sieve | D:\development\trunk\demo\demo\arm64\compiler\arm\sieve.c | 645--645 |
| P:2E000DC4-2E000DCB | \sieve_a64\sieve | D:\development\trunk\demo\demo\arm64\compiler\arm\sieve.c | 649--650 |

Two detached blocks of assembler code were generated for the source code at line 645.

If you want to inspect this in detail, the following commands might be helpful:

```
List.Hll ; display the source code in HLL
          ; mode (source order)

List.Mix /Track ; display the source code in Mixed
                  ; mode (target line order)
```

The expansion of the tree button shows how many detached blocks of assembler code are generated for the HLL line.

The expansion of the tree button shows how many detached blocks of assembler code are generated for the HLL line.

Export of Program Listing

In addition, the program listing can be exported with [List.EXPORT](#). The export is primarily utilized by the TRACE32 Coverage Report Utility to generate code coverage reports for the ObjectCode coverage metric. see “[Appendix A: TRACE32 Coverage Report Utility](#)” in Application Note for Trace-Based Code Coverage, page 117 (app_code_coverage.pdf).

General Options of the List Commands

| | |
|----------------|--|
| Mark | The Mark option highlights individual lines, depending on the breakpoint type. |
| MarkPC | The MarkPC option highlights all HLL source lines belonging to the current PC. See example . |
| DIVERGE | This option is mainly intended for internal diagnostic purposes: It attaches tags to executed and not executed ASM and HLL lines. The tags are displayed in the scale area of the List.* windows. You can open the scale area by dragging the slider control to the right. <ul style="list-style-type: none"> • For information about the tags, see example. • See also Step.Diverge. |

| | |
|----------------------------------|---|
| COverage | Display source listing together with code coverage information. Two examples are provided below: <ul style="list-style-type: none">• Object code coverage example.• MCDC coverage example. |
| CACHE | Displays cache hit information and marks currently cached code. |
| Track | Tracks the window to the reference position of other windows. The window tries first to track to the PROGRAM reference, and if this reference is not valid, it tracks to the DATA reference. |
| TOrder | List source lines in target line order . This is the default for assembly and mixed mode displays. |
| SOrder | List source lines in source line order . This is the default for source level displays. |
| ISTAT [<parameter>] | Display source listing together with the information provided by the instruction trace database (ISTATistic.ListFunc). Instructions that have not been executed are highlighted in yellow. <ul style="list-style-type: none">• For a description of the <parameters>, see table below.• ISTAT used without parameter, see example.• ISTAT used with the COverage parameter, see example. |

| | |
|-----------------|---|
| DEFault | Display the default information provided by the ISTAT database. |
| ALL | Display all information provided by the ISTAT database. |
| CLOCK | Display the clock and CPI information provided by the ISTAT database. |
| TCLOCK | (only for special purposes) |
| SAMPLES | Display recorded samples, time and ratio. |
| COVerage | Display the code coverage information provided by the ISTAT database. |

General Examples

Example for the COVerage option - ObjectCode coverage

List.auto MultiLine /COVerage

The screenshot shows the Lauterbach debugger interface with the command `List.auto MultiLine /COVerage` entered in the top bar. The assembly code window displays several lines of assembly code, each with a color-coded background indicating its coverage status:

- Lines 365, 368, 370, 371, 375, and 376 have a yellow background, indicating they are not fully covered (not taken).
- Lines 367, 369, 372, and 373 have a white background, indicating they are fully covered (ok).
- Other lines like 365, 368, 370, etc., also have white backgrounds.

```

[B:B:List.auto MultiLine /COVerage]
true false coverage    addr/line      code      label      mnemonic      comment
ok          365 static unsigned MultiLine(struct Compound *compound)
ok          P:900005DA 4F40           call      0x90000014 ; t32_alpha
ok          ok          Multiline:mov16.aa a15,a4      ; a15,compound
ok          ok          191E           if       (
ok          ok          P:900005E4  FD1CF6D        call      0x90000014 ; t32_alpha
ok          ok          FF54           ld16.w   d15,[a15]
ok          ok          P:900005E0  1F48           jeq16   d15,#0x1,0x900005F4
ok          ok          P:900005E2  191E           jne16   d15,#0x1,0x90000614
ok          ok          368           || compound->b == TRUE
ok          ok          P:900005E4  FD18FF6D        call      0x90000014 ; t32_alpha
ok          ok          P:900005E8  1F48           ld16.w   d15,[a15]0x4
ok          ok          P:900005E0  151E           jeq16   d15,#0x1,0x900005F4
ok          ok          369           || compound->c == TRUE
ok          ok          P:900005EC  FD14FF6D        call      0x90000014 ; t32_alpha
ok          ok          P:900005F0  2F48           ld16.w   d15,[a15]0x8
ok          ok          P:900005F2  11DE           jne16   d15,#0x1,0x90000614
ok          ok          370           && ( compound->d == TRUE
ok          ok          P:900005F4  FD10FF6D        call      0x90000014 ; t32_alpha
ok          ok          P:900005F8  3F48           ld16.w   d15,[a15]0x0C
ok          ok          P:900005F0  191E           jeq16   d15,#0x1,0x9000060C
ok          ok          371           || compound->e == TRUE
ok          ok          never          P:900005FC  FD0CFF6D        call      0x90000014 ; t32_alpha
ok          ok          never          P:90000600  4F48           ld16.w   d15,[a15]0x10
ok          ok          never          P:90000602  151E           jeq16   d15,#0x1,0x9000060C
ok          ok          372           || compound->f == TRUE))
ok          ok          never          P:90000604  FD08FF6D        call      0x90000014 ; t32_alpha
ok          ok          never          P:90000608  5F48           ld16.w   d15,[a15]0x14
ok          ok          never          P:9000060A  155E           jne16   d15,#0x1,0x90000614
ok          ok          373           return TRUE;
ok          ok          P:9000060C  FD04FF6D        call      0x90000014 ; t32_alpha
ok          ok          P:90000610  1282           mov16   d2,#0x1
ok          ok          P:90000612  033C           j16      0x90000618
ok          ok          375           }
ok          ok          P:90000614  0282           mov16   d2,#0x0
ok          ok          P:90000616  013C           j16      0x90000618
ok          ok          376           }
ok          ok          P:90000618  9000           ret16

```

Object code statements that are not tagged with 'ok' (indicating full coverage) are highlighted in bold with a yellow background.

Example for the COverage option - MCDC coverage

List.auto ComplexDoWhile /COverage

The screenshot shows the TRACE32 debugger interface with the title bar "B:List ComplexDoWhile /COverage". The main window displays assembly code for a "ComplexDoWhile" function, showing the flow of execution through various statements (stmt), decisions (dc), and branches (if, do, while). The code includes memory addresses, opcodes, and comments. The left margin shows decision points numbered 1. through 4. with corresponding true/false conditions. The right margin shows labels and memory addresses. The assembly code includes initialization of variables, a loop counter, and a loop body that increments the counter and checks a condition before exiting the loop.

| id | dec/cond | true | false | coverage | addr/line | code | label | mnemonic | comment | |
|----|----------|------|-------|----------|-----------|------------|--|------------------------|-------------------------------|---|
| 21 | 1. | 1. | 1. | 1. | stmt | 226 | static unsigned ComplexDoWhile(int const a, int const b, int const c, int const d) | | | |
| | | | | | ok | P:900004DA | 8810450B | ComplexD..:mov | e8,d5,d4 | ; e8,b,a |
| | | | | | ok | P:900004DE | 6A02 | mov16 | d10,d6 | ; d10,c |
| | | | | | ok | P:900004E0 | 7F02 | mov16 | d15,d7 | ; d15,d |
| 21 | 1. | 1. | 1. | 1. | stmt | 228 | unsigned num_cycles = 0u; | | | |
| | | | | | ok | P:900004E2 | 0B82 | mov16 | d11,#0x0 | |
| | | | | | dc | P:900004E4 | 231 | do { | | |
| | | | | | ok | P:900004E8 | 80032BBF | if (num_cycles > 1u) { | jlt.u | d11,#0x2,0x900004EA ; num_cycles,,0x900004EA |
| 22 | 1. | 1. | 1. | 1. | stmt | 232 | break; | j16 | 0x9000050E | |
| | | | | | ok | P:900004E8 | 133C | } | | |
| | | | | | stmt | P:900004E4 | 234 | num_cycles++; | add16 | d11,#0x1 ; num_cycles,#1 |
| | | | | | ok | P:900004E4 | 18C2 | } | | |
| 22 | 1. | 1. | 1. | 1. | dc | 236 | while (((!(Identity(a) >= -45) && Identity(b)) && Identity(c)) d); | | | |
| | | | | | ok | P:900004EC | 8402 | mov16 | d4,d8 | ; d4,a |
| | | | | | ok | P:900004EE | 0060006D | call | 0x900005AE | ; Identity |
| | | | | | ok | P:900004F2 | 0EF0303B | mov | d0,-#0x2D | |
| 22 | 2. | 1. | 1. | 1. | ok | P:900004F6 | 000B027F | jge | d2,d0,0x9000050C | |
| | | | | | ok | P:900004FA | 9402 | mov16 | d4,d9 | ; d4,b |
| | | | | | ok | P:900004F8 | 0059006D | call | 0x900005AE | ; Identity |
| | | | | | ok | P:90000502 | A402 | jz16 | d2,0x9000050C | |
| 22 | 3. | 1. | 1. | 1. | ok | P:90000504 | 0055006D | mov16 | d4,d10 | ; d4,c |
| | | | | | ok | P:90000508 | FFEE02DF | call | 0x900005AE | ; Identity |
| | | | | | ok | P:9000050C | ECEE | jne | d2,#0x0,0x900004E4 | |
| | | | | | ok | P:9000050C | ECEE | jnz16 | d15,0x900004E4 ; d,0x900004E4 | |
| | | | | | stmt | 238 | return num_cycles; | | | |
| | | | | | ok | P:9000050E | B202 | mov16 | d2,d11 | ; d2,num_cycles |
| | | | | | stmt | 239 | } | | | |

TRACE32 assigns an ID to each decision, with the atomic conditions that comprise the decision being numbered sequentially. Ideally, each condition is represented at the object code level by a conditional branch, as illustrated in the screenshot above.

Example for the ISTAT option - without <parameter>

[Back]

```
List.Asm func13 /ISTAT ; list instruction run-time  
; statistic
```

The screenshot shows the DIABC debugger interface. The assembly code for `func13` is displayed on the right, and execution statistics are shown on the left. The statistics table has three columns: address, value, and count. The first two rows show the total number of executions (466) and total clocks (469). The third row shows the average clocks per instruction (cpi).

| | count | clocks |
|------|------------|--------|
| 112. | 9882. 12.6 | 466 |
| 112. | 1412. 12.6 | 469 |

| | |
|---------------|--|
| count | Total number of instruction executions |
| clocks | Total number of clocks for the instruction |
| cpi | Average clocks per instruction |

Example for the ISTAT option - with the COverage <parameter>

[\[Back\]](#)

List func11 /ISTAT COverage ; list instruction coverage

| | | | exec | notexec | coverage | addr/line | code | label | mnemonic | comment | |
|------|-----|----------|------|---------|----------|-------------|----------|--------------|-------------------------|---------------------------|-------|
| 140. | 0. | 100.000% | | | | SP:20000BFO | 9421FFF0 | func11: | stwu | r1,-0x10(r1) ; r1,-16(r1) | |
| 28. | 0. | 100.000% | | | | SP:20000BF4 | 7C0802A6 | | mflr | r0 | |
| 28. | 0. | 100.000% | | | | SP:20000BF8 | 93E1000C | stw | r31,0x0C(r1) ; x,12(r1) | | |
| 28. | 0. | 100.000% | | | | SP:20000BFC | 90010014 | stw | r0,0x14(r1) ; r0,20(r1) | | |
| 28. | 0. | 100.000% | | | | SP:20000C00 | 7C7F1B78 | mr | r31,r3 ; x,r3 | | |
| 140. | 56. | 30.769% | | | | | 439 | switch (x) | | /* multiple returns */ | |
| 28. | 0. | 100.000% | | | | SP:20000C04 | 2C1F0003 | cmpwi | r31,0x3 | ; x,3 | |
| 28. | 0. | 0.000% | | | | SP:20000C08 | 4181001C | bgt | 0x20000C24 | ; .L341 (-) | |
| 0. | 0. | 0.000% | | | | SP:20000C0C | 41820044 | beq | 0x20000C50 | ; .L328 (-) | |
| 0. | 0. | 0.000% | | | | SP:20000C10 | 2C1F0001 | cmpwi | r31,0x1 | ; x,1 | |
| 0. | 0. | 0.000% | | | | SP:20000C14 | 41820024 | beq | 0x20000C38 | ; .L326 (-) | |
| 0. | 0. | 0.000% | | | | SP:20000C18 | 2C1F0002 | cmpwi | r31,0x2 | ; x,2 | |
| 0. | 0. | 0.000% | | | | SP:20000C1C | 4182002C | beq | 0x20000C48 | ; .L327 (-) | |
| 0. | 0. | 0.000% | | | | SP:20000C20 | 48000050 | b | 0x20000C70 | ; .L325 | |
| 28. | 0. | 100.000% | | | | SP:20000C24 | 2C1F0004 | .L341: | cmpwi | r31,0x4 | ; x,4 |
| 0. | 28. | 0.000% | | | | SP:20000C28 | 41820030 | beq | 0x20000C58 | ; .L329 (-) | |
| 28. | 0. | 100.000% | | | | SP:20000C2C | 2C1F0006 | cmpwi | r31,0x6 | ; x,6 | |
| 0. | 28. | 0.000% | | | | SP:20000C30 | 41820038 | beq | 0x20000C68 | ; .L331 (-) | |
| 28. | 0. | 100.000% | | | | SP:20000C34 | 4800003C | b | 0x20000C70 | ; .L325 | |

| | |
|-----------------|--|
| exec | conditional instructions: number of times the instruction was executed because the condition was true. other instructions: number of times the instruction was executed |
| notexec | conditional instructions: number of times the instruction wasn't executed because the condition was false. |
| coverage | Instruction coverage |

If exec or/and notexec is 0 for an instruction with condition, the instruction is bold-printed on a yellow background. All other instruction are bold-printed on a yellow background if they were not executed.

```
List ; display source listing around the
      ; current PC

List /Mark Program ; display source listing, bold print
                   ; all instructions / HLL lines on a
                   ; yellow background if a program
                   ; breakpoint is set

List /Mark ; remove bold printing on yellow
           ; background
```

```
List /Track           ; track the window to a reference, e.g.  

                     ; analyzer  

List Register(a0)    ; follow the register A0 of the CPU
```

Example for the DIVERGE option

[\[Back\]](#)

| s | state | i | addr | line | source |
|---|-------|---|------|------|---------------------|
| h | stop | | 822 | | k = i + prime; |
| h | done | | 823 | | while (k <= SIZE) { |
| | hit | | 823 | | while (k <= SIZE) { |
| | | | 824 | | flags[k] = FALSE; |
| | | | 825 | | k += prime; |
| | | | | 827 | } |
| | | | | | count++; |

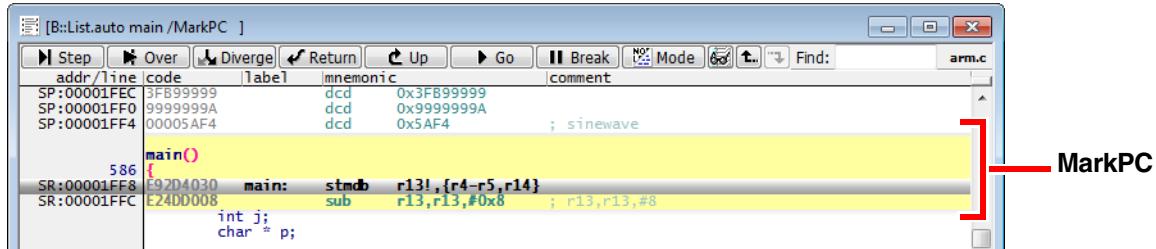
Tags in the columns s, state, and i

| | |
|---------------|--|
| h | Line in HLL mode (Mode.Hll). |
| a | Address in ASM mode (Mode.Asm). |
| stop | Reached by a breakpoint. |
| done | An executed line or address. |
| hit | A reached line or address that has not yet been executed, e.g. in a linear code sequence. |
| target | This line or address is a possible target of the next Step.Diverge . Once reached, target is replaced with hit . |
| i | Indirect branch instruction. |

Example for the MarkPC option

[\[Back\]](#)

```
Register.Set PC main      ; set the Program Counter to the label main
List.auto main /MarkPC   ; highlight all instructions related to the
                          ; current HLL line
```



See also

- | | | |
|--------------------|---------------------|------------------------|
| ■ List.Asm | ■ List.EXPORT | ■ List.HII |
| ■ List.Java | ■ List.Mix | ■ Data.LOAD |
| ■ Go | ■ SETUP.DIS | ■ SETUP.sYmbol |
| ■ SETUP.TIMEOUT | ■ Step | □ ADDRESS.isPHYSICAL() |
| □ ADDRESS.OFFSET() | □ ADDRESS.SEGMENT() | □ ADDRESS.STRACCESS() |
| □ sYmbol.END() | □ sYmbol.EXIT() | □ ADDRESS.WIDTH() |
- ▲ 'Release Information' in 'Legacy Release History'

Format: **List.auto** [<address> | <range>] [/<option>]

<option>: **Mark** <break>
 Flag <flag> (EF)
 DIVERGE
 COverage
 CACHE
 Track
 TOrder | SOrder
 ISTAT [<parameter>]

<flag>: **Read** | **Write** | **NoRead** | **NoWrite**

<break>: **Program** | **HII** | **Spot** | **Read** | **Write** | **Alpha** | **Beta** | **Charly** | **Delta** | **Echo**

<parameter>: **DEFault** | **ALL** | **CLOCKS** | **TCLOCKS** | **SAMPLES** | **COverage**

The display format of the **List.auto** window (assembler, mixed, or HLL) is dynamically determined by the current debug mode. You can change the debug mode by clicking the **Mode** button in the local toolbar of any List window or by using the **Mode** command.

| | |
|----------|--|
| <option> | For a description of the options, see “ General Options of the List Commands ”, page 30. |
|----------|--|

See also

- | | | | |
|---|---------------------------------------|--|------------------------------------|
| ■ List | ■ List.Asm | ■ List.EXPORT | ■ List.HII |
| ■ List.Java | ■ List.Mix | ■ SETUPDIS | ■ SETUPLISTCLICK |
| ■ SETUP.sYmbol | ■ SETUP.TIMEOUT | □ ADDRESS.isPHYSICAL() | □ ADDRESS.OFFSET() |
| □ ADDRESS.SEGMENT() | □ ADDRESS.STRACCESS() | □ ADDRESS.WIDTH() | □ sYmbol.END() |
| □ sYmbol.EXIT() | | | |
| ▲ ‘Release Information’ in ‘Legacy Release History’ | | | |

Format: **List.Asm** [<address>] [/<option>]

<option>:

- Mark** <break>
- Flag** <flag> (EF)
- COverage**
- CACHE**
- Track**
- MarkPC**

TOrder | SOrder
IgnoreSymbols

Displays the program in disassembled format.

| | |
|----------------------|---|
| <option> | For a description of the options, see “ General Options of the List Commands ”, page 30. |
| IgnoreSymbols | Let the disassembler ignore any symbols for deciding at which byte of the machine code the disassembling should start. This option does normally only make sense on architectures with different instruction lengths. |

See also

- | | | | |
|----------------------------|-----------------------------|-------------------------------|-----------------------------|
| ■ List | ■ List.auto | ■ List.HII | ■ List.Java |
| ■ List.Mix | ■ Mode | □ DEBUGMODE() | |

Using the **List.EXPORT** command group, you can export a source or disassembler listing or both listings to an XML file.

In addition, TRACE32 provides an XSL transformation template for formatting the XML file. The formatting is automatically applied to the XML file when it is opened in an external browser window. Prerequisite: The XSL file is placed in the same folder as the XML file.

For demo scripts, see [List.EXPORT.Mix](#).

See also

- | | | | |
|--|--|---|---|
| <ul style="list-style-type: none">■ List.EXPORT.Asm■ List | <ul style="list-style-type: none">■ List.EXPORT.auto■ List.auto | <ul style="list-style-type: none">■ List.EXPORT.HII■ COVerage.EXPORT | <ul style="list-style-type: none">■ List.EXPORT.Mix■ ISTATistic.EXPORT |
|--|--|---|---|

List.EXPORT.Asm

Export disassembler listing

Format: **List.EXPORT.Asm <file> <range> [/<option>]**

<option>: **COVerage | ISTATistic Append | NoData**

Creates an XML file containing the disassembler listing. For an example and a description of the options, see [List.EXPORT.Mix](#).

See also

- [List.EXPORT](#)

List.EXPORT.auto

Export source and disassembler listing

Format: **List.EXPORT.auto <file> <range> [/<option>]**

<option>: **COVerage | ISTATistic | TOrder | SOrder Append | NoData | COVerableItems**

Creates an XML file containing the source listing and the disassembler listing - same as [List.EXPORT.Mix](#). For an example and a description of the options, see [List.EXPORT.Mix](#).

See also

- [List.EXPORT](#)

| | |
|-----------|--|
| Format: | List.EXPORT.HII <file> <range> [/<option>] Data.ListEXPORTHII (as an alternative) |
| <option>: | COVerage ISTATistic Append NoData |

Creates an XML file containing just the source listing. For an example and a description of the options, see [List.EXPORT.Mix](#).

See also

■ [List.EXPORT](#)

List.EXPORT.Mix

Export source and disassembler listing

[[Example](#)]

| | |
|-----------|--|
| Format: | List.EXPORT.Mix <file> <range> [/<option>] |
| <option>: | COVerage ISTATistic SOrder TOrder Append NoData COVerableItems |

Creates an XML file containing the source listing and the disassembler listing.

<file> Name of the XML file that stores a listing of the source and disassembler code. The file extension *.xml can be omitted.

<range> Address filter for exporting the specified range.

Append Appends the listing to an existing XML file - without overwriting the current file contents.

COVerage Listing additionally contains code coverage information.

COVerableItems Exports only coverage results for measurable source lines.

ISTATistic Listing additionally contains information provided by the ISTATistics module.

NoData Excludes data-only sections from the XML output.

SOrder Export the source code lines in [source order](#).

TOrder Export the source code lines in [target order](#) (default).

Example 1: The prerequisites for the following example are that the debug symbols have already been loaded, the address bookmarks have been created, and trace data has been recorded.

```
COverage.ADD           ;update the coverage database
Data.List /COverage /Track ;display source listing
COverage.ListFunc       ;display coverage for HLL functions

;export all bookmarks
BookMark.EXPORT "~~/list.xml"

;export the source listing of the functions "main" and "sieve"
List.EXPORT.Mix "~~/list.xml" main /COverage /Append
List.EXPORT.Mix "~~/list.xml" sieve /COverage /Append

;for demo purposes: let's open the unformatted result in TRACE32
EDIT "~~/list.xml"

;place the transformation template in the same folder as the XML file
COPY "~~/demo/coverage/single_file_report/t32transform.xsl" \
      "~~/t32transform.xsl"

;you can now open the formatted result in an external browser window
OS.Command start iexplore.exe "file:///C:/t32/list.xml"
```

The tildes ~~ expand to your TRACE32 system directory, by default c:\t32.

Example 2: A more complex demo script is included in your TRACE32 installation. To access the script, run this command:

```
B:::CD.PSTEP ~/demo/coverage/example.cmm
```

See also

- [List.EXPORT](#)

Format: **List.HII** [*<address>*] [/<*option*>]

<*option*>: **Mark** <*break*>
Flag <*flag*> (EF)
COverage
CACHE
Track
MarkPC
TOrder | **SOrder**

Displays the program in source format. If the starting address in not an HLL (High Level Language) line, assembler code is displayed to the next source code line found in the code segment.

| | |
|-----------------------|--|
| <i><option></i> | For a description of the options, see “ General Options of the List Commands ”, page 30. |
|-----------------------|--|

See also

- | | | | |
|---|---|---|---|
| <ul style="list-style-type: none">■ List■ List.Mix | <ul style="list-style-type: none">■ List.Asm■ Mode | <ul style="list-style-type: none">■ List.auto□ DEBUGMODE() | <ul style="list-style-type: none">■ List.Java |
|---|---|---|---|

List.Java

Display Java byte code

[\[Example\]](#)

Format: **List.Java** [*<address>*] [/<*option*>]

<*option*>: **Mark** <*break*>
Flag <*flag*>(EF)
COverage
CACHE
Track
TOrder | **SOrder**

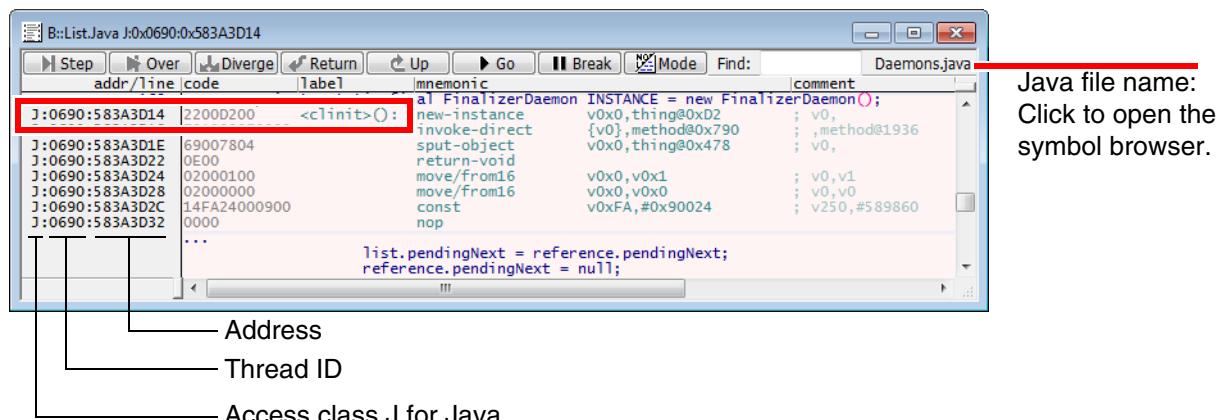
Displays the program in Java byte code format. The functionality is the same as the [List.auto](#) command.

This command is NOT required when an address range is specified as Java byte code area with the Java byte code debugger or when the processor implements a special byte code mode (e.g. ARM Jazelle).

| | |
|-----------------------|--|
| <i><option></i> | For a description of the options, see “ General Options of the List Commands ”, page 30. |
|-----------------------|--|

Android is used in this example:

```
;list all running tasks by magic number, task name, task ID, etc.  
TASK.List.tasks  
  
;change view to a task by specifying the magic number, task name or ID  
Frame.TASK 0xEFDD29700  
  
;display a source listing at this address for the task specified above  
List.Java J:0x0690:0x583A3D14  
  
;alternatively, use the label to display the same source listing.  
List.Java `<clinit>()` ;enclose the label in backticks `...`
```



See also

-
- [List](#)
 - [List.Asm](#)
 - [List.auto](#)
 - [List.Hll](#)
 - [List.Mix](#)

Format: **List.Mix [<address>] [/<option>]**

<option>: **Mark <break>**
Flag <flag> (EF)
COverage
CACHE
Track
MarkPC
TOrder | SOrder

The code is displayed in HLL (High Level Language) and additionally disassembled from the memory.

| | |
|----------|--|
| <option> | For a description of the options, see “ General Options of the List Commands ”, page 30. |
|----------|--|

See also

- | | | | |
|-----------------------------|----------------------------|-------------------------------|----------------------------|
| ■ List | ■ List.Asm | ■ List.auto | ■ List.Hll |
| ■ List.Java | ■ Mode | □ DEBUGMODE() | |

LOGGER

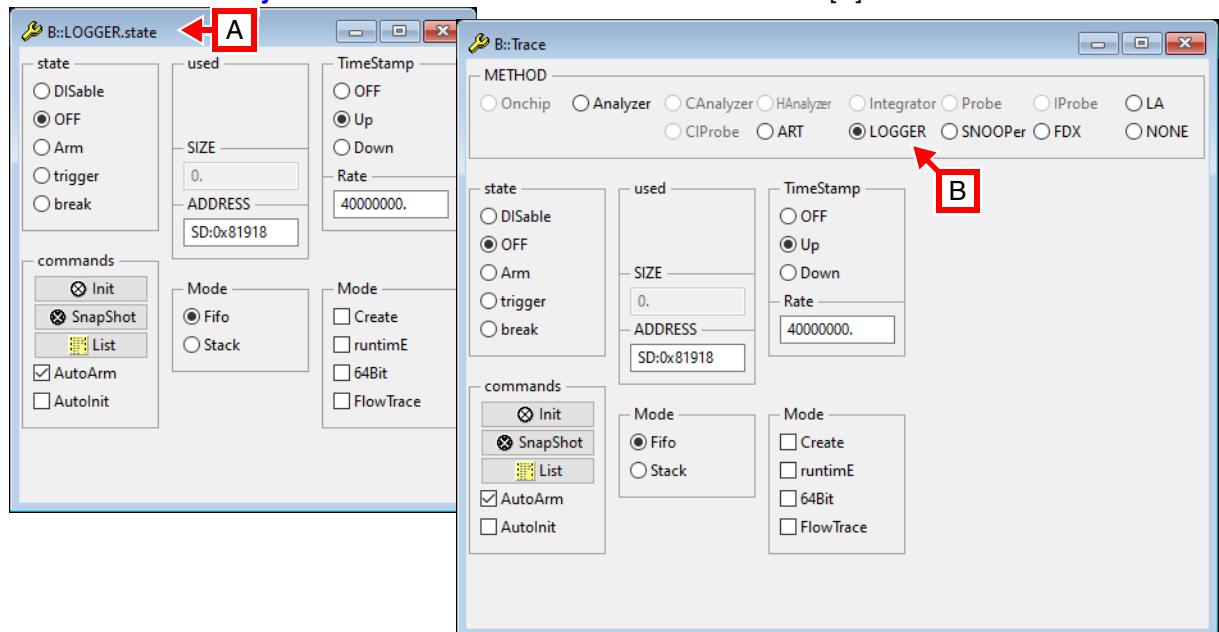
Trace method LOGGER, recording and analysis commands

LOGGER is a software trace method where the target application writes the required trace information to a reserved buffer on the target RAM. TRACE32 loads then the trace information from the target RAM for display and processing.

Please refer to the “[Application Note for the LOGGER Trace](#)” (app_logger.pdf) for more information.

For selecting and configuring the trace method LOGGER, use the TRACE32 command line or a PRACTICE script (*.cmm) or the **LOGGER.state** window [A].

Alternatively, use the **Trace.state** window: click the option **LOGGER** or execute the command **Trace.METHOD Analyzer** in order to select the trace method **LOGGER** [B].



The chapter “[LOGGER-specific Trace Commands](#)”, page 47 describes the LOGGER-specific configuration commands. While the chapter “[Generic LOGGER Trace Commands](#)”, page 49 lists the LOGGER trace analysis and display commands, which are shared with other TRACE32 trace methods.

See also

■ [FDX](#)

■ [Trace.METHOD](#)

LOGGER-specific Trace Commands

LOGGER.ADDRESS

Software trace address

Format: **LOGGER.ADDRESS [<address>]**

Defines the address of the logger trace control block in target memory.

LOGGER.Mode

Set LOGGER operation mode

Format: **LOGGER.Mode [<mode>]**

<mode>: **Fifo | Stack**

**Create
runtimE
64bit**

FlowTrace

Selects the trace operation mode.

| | |
|------------------|--|
| Fifo | If the trace is full, new records will overwrite older records. The trace records always the last cycles before the break. |
| Stack | If the trace is full recording will be stopped. The trace always records the first cycles after starting the trace. |
| Create | Create LOGGER software trace control block by debugger. |
| runtimE | Dualport access. |
| 64Bit | LOGGER mode for 64-bit traces. |
| FlowTrace | Special mode where the LOGGER is used to sample the program flow. The LOGGER trace listing reconstructs the program flow based on the sampled information. Only supported for PowerPC and SH4. |

See also

- [**<trace>.Mode**](#)

Format:

LOGGER.TimeStamp OFF | Up | Down | Rate <rate>

Configure timestamps for the **LOGGER** trace. The LOGGER trace record format includes a timestamp field for up to 48 bit timestamps. The direction and rate information passed by this command is required to convert the timestamp into the time in seconds.

| | |
|---|--|
| OFF (default) | Disable timestamps. Use this setting if the LOGGER target code does not store timestamps in the LOGGER trace records. When this setting is used, the x-direction in chart views is the record number axis instead of the time axis. |
| Up | Enable timestamp counter, counting upwards. Use this setting if the LOGGER target code stores timestamps in the LOGGER trace records and if the timestamp increments with each timer tick. |
| Down | Enable timestamp counter, counting downwards. Use this setting if the LOGGER target code stores timestamps in the LOGGER trace records and if the timestamp decrements with each timer tick. |
| Rate <rate> | Frequency of the timestamp in ticks per second. |
| AllCycles [ON OFF] SH only | Set timestamp generation frequency. <ul style="list-style-type: none">• OFF (default): Generate a single timestamp for 6 trace cycles.• ON: Generate dedicated timestamps for all trace cycles. |

Example: The timestamp used by the LOGGER target code increments at a rate of 16 million per second (16 MHz):

```
LOGGER.TimeStamp Up
LOGGER.TimeStamp Rate 16000000.
```

Generic LOGGER Trace Commands

LOGGER.ACCESS Define access path to program code for trace decoding

See command [**<trace>.ACCESS**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 131).

LOGGER.Arm Arm the trace

See command [**<trace>.Arm**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 134).

LOGGER.AutoArm Arm automatically

See command [**<trace>.AutoArm**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 135).

LOGGER.AutoInit Automatic initialization

See command [**<trace>.AutoInit**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 140).

LOGGER.BookMark Set a bookmark in trace listing

See command [**<trace>.BookMark**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 140).

LOGGER.BookMarkToggle Toggles a single trace bookmark

See command [**<trace>.BookMarkToggle**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 143).

See command [**<trace>.Chart**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 144).

LOGGER.ComCompareCompare trace contents

See command [**<trace>.ComCompare**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 192).

LOGGER.DISableDisable the trace

See command [**<trace>.DISABLE**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 197).

LOGGER.DRAWPlot trace data against time

See command [**<trace>.DRAW**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 201).

LOGGER.EXPORTExport trace data for processing in other applications

See command [**<trace>.EXPORT**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 212).

LOGGER.FILELoad a file into the file trace buffer

See command [**<trace>.FILE**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 233).

LOGGER.FindFind specified entry in trace

See command [**<trace>.Find**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 235).

LOGGER.FindAllFind all specified entries in trace

See command [**<trace>.FindAll**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 237).

See command [**<trace>.FindChange**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 238).

See command [**<trace>.FLOWPROCESS**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 241).

See command [**<trace>.FLOWSTART**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 241).

See command [**<trace>.GOTO**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 244).

See command [**<trace>.Init**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 246).

See command [**<trace>.List**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 248).

See command [**<trace>.ListNesting**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 263).

See command [**<trace>.ListVar**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 266).

See command [**<trace>.LOAD**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 270).

See command [**<trace>.OFF**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 278).

See command [**<trace>.PROfileChart**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 283).

See command [**<trace>.PROfileSTATistic**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 322).

See command [**<trace>.PROTOcol**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 339).

See command [**<trace>.PROTOcol.Chart**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 339).

See command [**<trace>.PROTOcol.Draw**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 341).

See command [**<trace>.PROTOcol.EXPORT**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 342).

See command [**<trace>.PROTOcol.Find**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 343).

See command [**<trace>.PROTOcol.list**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 344).

See command [**<trace>.PROTOcol.PROfileChart**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 347).

See command [**<trace>.PROTOcol.PROfileSTATistic**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 348).

See command [**<trace>.PROTOcol.STATistic**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 350).

See command [**<trace>.REF**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 357).

LOGGER.RESet

Reset command

See command [**<trace>.RESet**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 357).

LOGGER.SAVE

Save trace for postprocessing in TRACE32

See command [**<trace>.SAVE**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 358).

LOGGER.SelfArm

Automatic restart of trace recording

See command [**<trace>.SelfArm**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 362).

LOGGER.SIZE

Define buffer size

See command [**<trace>.SIZE**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 373).

LOGGER.SnapShot

Restart trace capturing once

See command [**<trace>.SnapShot**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 373).

LOGGER.state

Display trace configuration window

See command [**<trace>.state**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 376).

LOGGER.STATistic

Statistic analysis

See command [**<trace>.STATistic**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 378).

See command [**<trace>.Timing**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 499).

LOGGER.TRACKSet tracking record

See command [**<trace>.TRACK**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 502).

LOGGER.ViewDisplay single record

See command [**<trace>.View**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 504).

LOGGER.ZEROAlign timestamps of trace and timing analyzers

See command [**<trace>.ZERO**](#) in 'General Commands Reference Guide T' (general_ref_t.pdf, page 505).

LUA

Support for the Lua script language

The TRACE32 Lua API allows the user to load and execute Lua scripts directly in the debugger. This feature can be used to accelerate execution of certain debug commands by avoiding the interaction between the TRACE32 host software and the debug driver. A Lua interpreter is built into the debug box, supporting the complete Lua language. Please refer to the official website of Lua www.lua.org for documentation.

In addition to the standard language elements, Lauterbach extended Lua with a set of TRACE32 specific libraries. This allows you to, for example, use the JTAG shift functions directly in the Lua script. For a description of the TRACE32 specific libraries, refer to “[TRACE32 Lua Library](#)” (lua_library.pdf).

The TRACE32 host software maintains an input buffer with size 0x1000 bytes to store input parameters for the Lua script. While sending the command to execute a certain Lua script, TRACE32 also packs and sends the input buffer to the debugger. Use [LUA.Data.SET](#) to manipulate the input buffer and

[LUA.Data.ShowInput](#) to view the current content in the input buffer. Loading the input buffer from a binary file is also supported. From within the Lua scripts, the data in the input buffer can be retrieved using functions from the “[TRACE32 Lua Library](#)” (lua_library.pdf).

When executing a Lua script, the TRACE32 host software blocks until it returns. Inside the Lua script, functions from the “[TRACE32 Lua Library](#)” (lua_library.pdf) can be used to store output data into an output buffer (again 0x1000 bytes). The output buffer will be automatically sent back to the TRACE32. Use [LUA.Data>ShowOutput](#) to view the output data. It is also supported to save the output buffer into a binary file.

The Lua API can be used both as TRACE32 commands and through the remote C API. The [LUA](#) command group is described in the following sections and the C API is introduced in chapter [Remote Lua API](#).

LUA.Data.Loadinput

Load content from a file into the input buffer

| | |
|---------|---|
| Format: | LUA.Data.Loadinput <file> [/<load_option>] |
|---------|---|

Load content from a binary file into the input buffer. Use [LUA.Data>ShowInput](#) to check the current content of the input buffer.

Offset
<offset>

Starting position in the binary file to be loaded into the input buffer

Length
<length>

Number of bytes to be loaded into the input buffer

Format: **LUA.Data.Saveoutput <file> [/<save_option>]**

Save content of the output buffer into a file. Use [LUA.Data>ShowOutput](#) to check the current content of the output buffer.

Append Append data to the end of the output file.

LUA.Data.SET

Modify the Lua input buffer

Format: **LUA.Data.SET <index> %<format> <value>**

<format>: **Byte | Word | Long | Quad | TByte | HByte | PByte | SByte
BE | LE**

Writes byte-wise data to the input buffer. Use [LUA.Data>ShowInput](#) to view the current content of the input buffer. The input buffer will be sent to the debugger upon executing a [LUA.Program.RUN](#) command.

| | |
|------------------------|---|
| <index> | The byte position of the input buffer to be written. |
| Byte, Word, ... | Data size. <ul style="list-style-type: none">• Byte (8-bit accesses) Word (16-bit accesses)• TByte (24-bit accesses) Long (32-bit accesses)• PByte (40-bit accesses) HByte (48-bit accesses)• SByte (56-bit accesses) Quad (64-bit accesses)• |
| BE, LE | Define byte endianness: big endian or little endian. |
| <value> | The value to be written to the buffer. |

Format: **LUA.Data.ShowInput**

Displays the current content of the input buffer in the **AREA** window.

Format: **LUA.Data.ShowOutput**

Displays the current content of the output buffer in the **AREA** window. The output buffer contains the return values from the Lua script. Inside the Lua script, functions from the “[TRACE32 Lua Library](#)” ([lua_library.pdf](#)) can be used to write to the output buffer.

Format: **LUA.Program.List**

Lists the Lua scripts that have been loaded into the debugger. The output is redirected to the **AREA** window and has the following format: `<id> : <label>, <attribute>, <file>`. See [example](#).

| | |
|------------------|--|
| ID | An auto-generated sequential index of the current Lua scripts. Note that the ID of a certain script may change after loading/unloading another script. |
| label | A unique string to identify a Lua script. |
| attribute | EXE: an executable script. LIB: a library script. See LUA.Program.LOAD for more details. |
| filename | Path to the Lua file. |

Format: **LUA.Program.LOAD** <file> [<label>] [/<options>]<option>: **Program | Library**

Loads the Lua script to the debugger without executing it. The script is uniquely identified by its label. See [example](#) here.

| | |
|----------------|---|
| <file> | Path and file name of the Lua script to be loaded. |
| <label> | A unique string to identify the Lua script. If not specified, a default label will be generated. |
| Program | The Lua script is loaded as an executable script. This is the default option. |
| Library | The Lua script is loaded as a library script. The Lua functions in a library script will be made available to all other Lua scripts. Although marked as a library, the script itself can still be executed normally. However, we recommend to develop separate Lua scripts for libraries and executables. |

LUA.Program.RESet

Reset the Lua context

Format: **LUA.Program.RESet**

Resets the Lua context, unloads all scripts from the debugger and clears all input and output buffers.

LUA.Program.RUN

Execute a Lua script

Format: **LUA.Program.RUN** <id> | <label>

Executes the Lua script with given index or label. Use [LUA.Program.List](#) to check IDs and labels of the Lua scripts that are currently available in the debugger.

See [example](#).

Format: **LUA.Program.UNLOAD <id> | <label>**

Removes a Lua script from the debugger. Use **LUA.Program.List** to see the scripts currently loaded.

Example

```
; load the Lua script lib.lua as a library
LUA.Program.LOAD c:\lua\lib.lua "mylib" /Library
; load the Lua script jtag.lua as an executable
; use default option "/Program" and default label
LUA.Program.LOAD c:\lua\jtag.lua

; see the current list of Lua scripts
LUA.Program.List
; you should see the following:
; 0 : mylib, LIB, C:\lua\lib.lua
; 1 : jtag.lua, EXE, C:\lua\jtag.lua

; set input parameter
LUA.Data.SET 0x0 %1 0x12345678
LUA.Data.ShowInput

; execute the Lua script jtag.lua using its index
LUA.Program.RUN 1
; execute the Lua script jtag.lua using its label
LUA.Program.RUN "jtag.lua"

; view the output buffer
LUA.Data.ShowOutput

; remove a Lua script
LUA.Program.UNLOAD 0
; Note that now the Lua script with index 0 (lib.lua) is removed
; and the indexing has changed
LUA.Program.List
; now you should see the following
; 0 : jtag.lua, EXE, C:\lua\jtag.lua
; the Lua script jtag.lua now has the index 0
LUA.Program.UNLOAD "jtag.lua"

; clear the context
LUA.Program.RESet
```