LAUTERBACH
DEVELOPMENT TOOLS

MODULAR, POWERFUL AND FUTURE PROVEN

Debugger & Trace Solutions for Infineon Microcontrollers

Infineon offers a comprehensive portfolio of Microcontrollers (MCUs) to address requirements across a wide set of industries and applications. They implement several kinds of TriCore™ and Arm® CPUs as well as specialized IP blocks for communication, power management and application acceleration.

Lauterbach's market leading TRACE32® debug and trace development tools provide not only full insights into all today's Infineon's chips for the whole SoC lifecycle: They also facilitate the path to certification for safety-critical applications in accordance with ISO 26262, DO-178 C, and other standards.

KEY FEATURES

Unlimited Multicore Debugging

Infineon Microcontroller implement different kinds of TriCore™ and Arm® Cortex CPUs. In addition several Infineon Chips implement companion cores, DSPs and/or hardware accelarators of different architectures. No matter what kind of multicore system is used, TRACE32® supports them all.

OS- und Hypervisor-Aware Debugging of Any Core

Lauterbach's TRACE32® OS-aware debugging provides key insights into applications and the operating systems they are running on. With this, engineers can better understand how they are behaving and utilizing chip resources. On virtualized systems, TRACE32® Hypervisor-aware debugging allows to perform concurrent OS-aware debugging for each virtual machine (VM) and display an overview of the overall system.

Simplifying Certification of Safety-Critical Applications

Lauterbach's certified Tool Qualification Support Kits (TQSK) provide everything developers need to qualify TRACE32® solutions according to ISO 26262, DO-178 C, IEC 61508, IEC 62304 and EN 50128. Different TQSK variants prove the suitability of code coverage, debugging, and instruction set simulator to reduce time-to-market, effort, and costs.

Covering the Whole Infineon Chip Lifecycle

Besides to real silicon, TRACE32® tools can connect to various simulators, emulators, and virtual targets. Developers can reuse the scripts generated in this phase throughout the entire product life cycle because the user interface and scripting commands stay the same from simulations through use in the field by the customers.

DOWNLOAD OUR SOLUTIONS OVERVIEW

All information about Lauterbach's products for debugging and tracing.

TRACE32®
SOLUTIONS OVERVIEW

#1 DEBUG & TRACE TOOLS

LEARN MORE @
lauterbach.com

INFINEON

DEBUGGER and TRACE-Solutions
for All Infineon Microcontrollers

CHIPS		Debug	On-Chip Trace	Off-Chip Trace	Debugging via CAN	XCP Debugging	Instruction Set Simulator
		AVAILABLE TRACE32® SOLUTIONS					
AURIX™ including TC4x, TC4Dx	TriCore™, Arm® Cortex-M*, GTM*, ARC* (PPU & cDSP), XC800*	✓ 1	✓* 1	✓* 3/4**	✓* 1+5/4+5	✓* 6	✓ 7
TRAVEO™ II	Arm® Cortex-M	✓ 1/2/4	✓ 1/2/4	✓ 2/4		✓ 6	✓ 7
PSoC™	Arm® Cortex-M	✓ 1/2/4	✓* 1/2/4	✓* 2/4		✓ 6	✓ 7
XMC	Arm® Cortex-M	✓ 1/2/4	✓* 1/2/4	✓* 2/4		✓ 6	✓ 7
...further Infineon Chips	Please search Lauterbach's chip database - see QR code below						

* : Availability depending on the sub series
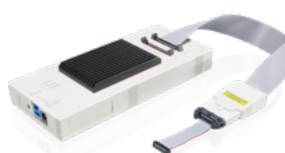** : Applicability depending on the sub series

Find the Right TRACE32® Solution for Your Chip:
lauterbach.com/supported-platforms/infineon
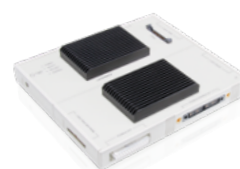
PowerDebug System

1 Powerful, moduler, flexible debug system for 150+ micro-processor families

µTrace®

2 All-in-one Debug- and Trace-Tool for Cortex®-M

PowerTrace System

3 Highest performance parallel and serial trace system

CombiProbe 2

4 Compact debug & trace probe for low-bandwidth trace capture

Debugging via CAN

5 Debugging over a CAN interface supporting DXCPL, DXCM and SSDP

XCP Debug & Trace

XCP

6 Software solution for debugging and onchip tracing via XCP protocol

Instruction Set Simulator

ISS Instruction Set Simulator

7 ISS for developing or testing application code without target hardware

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