

MODULAR, POWERFUL AND FUTURE PROVEN

Debugger & Trace Solutions for NCP Microprocessors & Microcontrollers

NXP offers a comprehensive portfolio of Microcontrollers (MCUs) and Microprocessors (MPUs) to address requirements across a wide set of industries and applications. They implement several kinds of Arm[®] and PowerPC CPUs as well as optional RISC-V and DSP cores and also specialized IP blocks for communication, power management and application acceleration.

Lauterbach's market leading TRACE32[®] debug and trace development tools provide not only full insights into all today's NXP's chips for the whole SoC lifecycle: Thanks to the long-standing close partnership with NXP, future chip developments are also accompanied by Lauterbach from the very beginning-ensuring a future proof investment.

Unlimited Multicore Debugging

NXP MPUs and MCUs implement different kinds of PowerPC and Arm[®] CPUs of the Cortex-A/R/M families. In addition several NXP SoCs implement RISC-V based companion cores, DSPs and/or hardware accelarators of different architectures. No matter what kind of multicore system is used, Lauterbach's TRACE32[®] tools support them all.

OS-Aware Debugging of Any Core

Lauterbach's TRACE32[®] OS-aware debugging provides key insights into applications and the operating systems they are running on, no matter if rich operating systems like Linux, real-time operating systems (RTOS), or a mixture of all is used. With this, engineers can better understand how they are behaving and utilizing chip resources.

DOWNLOAD OUR SOLUTIONS OVERVIEW



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All information about Lauterbach's products for debugging and tracing.

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Supporting the Full NXP S32 Automotive Platform

Lauterbach's TRACE32[®] debug and trace development tools fully support NXP's S32 automotive platform including the sophisticated S32N5x super vehicle integration processor series. For the latter, Lauterbach is the only hardware and software debugger supplier listed on NXP's corresponding S32N55 product website, enabling the SDV revolution.

Covering the Whole NXP Chip Lifecycle

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Besides to real silicon, TRACE32[®] tools can connect to various simulators, emulators, and virtual targets. Developers can reuse the scripts generated in this phase throughout the entire product life cycle because the user interface and scripting commands stay the same from simulations through use in the field by the customers.

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NXP-MCUs/MPUs



DEBUGGER and TRACE-Solutions for All NXP MCUs and MPUs

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CHIPS		AVAILABLE TRACE32® SOLUTIONS				
S32 including S32N5x series	Arm [®] Cortex, GTM*, CEVA-X*, XTENSA*, eTPU*, SPT*, CoolFlux*, VSPA*	√ 1/2**	√ [*] 1/2**	√ [*] 2**/3/4	√ 5	√ 6
i.MX including i.MX RT series	Arm [®] Cortex, XTENSA*, RISC-V*,SDMA*	√ 1/2**	✓ [*] 1/2**	[*] 2 ^{**} /3/4 [*]	√ 5	√ 6
LPC, Kinetis [®] , K32W	Arm [®] Cortex-M	√ 1/2	√ [*] 1/2	√ [*] 2/4	√ 5	√ 6
мсх	Arm [®] Cortex-M, Coolflux*	√ 1/2**	√ [*] 1/2**	✓ [*] 2**/4	√ 5	√ 6
Layerscape®	Arm [®] Cortex	√ 1	√ [*] 1	√* з	√ 5	√ 6
MPC5x	PowerPC, eTPU*, GTM*, SPT*	√ 1	√ [*] 1	√* з	√ 5	√ 6
QorlQ [®]	PowerPC	√ 1	√ [*] 1	√ [*] 3	_	√ 6
further NXP Chips	Please search Lauterbach's chip database - see QR code below					

* : Availability depending on the sub series

**: Applicability depending on the sub series



Find the Right TRACE32[®] Solution for Your Chip: lauterbach.com/supported-platforms/nxp



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