LAUTERBACH
DEVELOPMENT TOOLS

MODULAR, POWERFUL AND FUTURE PROVEN

Debugger & Trace Solutions for All Arm® Cortex & Neoverse CPUs

Arm® offers a comprehensive portfolio of IP cores to address requirements across a wide set of industries and applications. They are licensed by all major semiconductor suppliers and implemented in countless chips from tiny microcontrollers to powerful multicore SoCs serving in data centers, mobile devices, vehicles, avionics & space applications, medical devices, robots and a wide range of other industrial applications.

Lauterbach's market leading TRACE32® debug and trace development tools provide not only full insights into all today's chips with Arm®-cores for the whole lifecycle: They also facilitate the path to certification for safety-critical applications in accordance with ISO 26262, DO-178 C, and other standards.

Thanks to the long-standing close partnership with Arm® and all major semiconductor suppliers, which implement Arm®-cores, future chip developments are also accompanied by Lauterbach from the very beginning ensuring a future proof investment.

arm

DOWNLOAD OUR SOLUTIONS OVERVIEW

All information about Lauterbach's products for debugging and tracing.

KEY FEATURES

Unlimited Multicore Debugging

Many MPUs and MCUs implement different kinds of Arm® CPUs of the Cortex-X/A/R/M and Neoverse-V/N/E families including automotive AE series as well as CoreSight IP which supports symmetric multiprocessing (SMP) as well as asymmetric multiprocessing (AMP) in heterogeneous SoCs. No matter what kind of multicore system is used, Lauterbach's TRACE32® tools support them all.

OS-Aware Debugging of Any Core

Lauterbach's TRACE32® OS-aware debugging provides key insights into applications and the operating systems they are running on, no matter if rich operating systems like Linux, real-time operating systems (RTOS), or a mixture of all is used. With this, engineers can better understand how they are behaving and utilizing chip resources.

Simplifying Certification of Safety-Critical Applications

Lauterbach's certified Tool Qualification Support Kits (TQSK) provide everything developers need to qualify TRACE32® solutions according to ISO 26262, DO-178 C, IEC 61508, IEC 62304 and EN 50128. Different TQSK variants prove the suitability of code coverage, debugging, and instruction set simulator to reduce time-to-market, effort, and costs.

Covering the Whole Arm®-Based Chip Lifecycle

Besides to real silicon, TRACE32® tools can connect to various simulators, emulators, and virtual targets. Developers can reuse the scripts generated in this phase throughout the entire product life cycle because the user interface and scripting commands stay the same from simulations through use in the field by the customers.

LEARN MORE @
lauterbach.com

Arm®

DEBUGGER and TRACE-Solutions
for All Arm® Cores

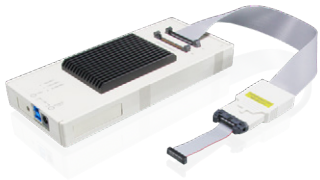
Chip-Family	Architectures	Debug	On-Chip Trace	Off-Chip Trace	XCP Debugging	Instruction Set Simulator
CHIPS		AVAILABLE TRACE32® SOLUTIONS				
Cortex®-Mx	Arm™6-M, Arm™7-M, Arm™8-M	✓ 1/2/4	✓ 1/2/4	✓ 2/3/4	✓ 5	✓ 6
Cortex®-Ax/Xx (AE)	Arm™7-A, Arm™8-A, Arm™9-A	✓ 1	✓ 1	✓ 3	✓ 5	✓ 6
Cortex®-Rx (AE)	Arm™7-R, Arm™8-R, Arm™9-R	✓ 1	✓ 1	✓ 3	✓ 5	✓ 6
Neoverse™-Vx/Nx/Ex (AE)	Arm™8-A, Arm™9-A	✓ 1	✓ 1	✓ 3	✓ 5	✓ 6
...further Arm® Cores	Please search Lauterbach's chip database – see QR code below					

Find the Right TRACE32® Solution for Your Chip:
lauterbach.com/supported-platforms/arm

PowerDebug System

1 Powerful, moduler, flexible debug system for 150+ microprocessor families

µTrace®



2 All-in-one Debug- and Trace-Tool for Cortex®-M

PowerTrace System

3 Highest performance parallel and serial trace system

CombiProbe 2

4 Compact debug & trace probe for low-bandwidth trace capture

XCP Debug & Trace

5 Software debugger via XCP measurement and calibration protocol

Instruction Set Simulator

ISS
Instruction Set Simulator

6 ISS for developing or testing application code without target hardware