

In-Circuit Emulator for Renesas H8/300 and H8/500

H8/3006	H8/3334Y	H8/536R
H8/3007	H8/3334YF	H8/536S
H8/3060	H8/3336Y	H8/537
H8/3061	H8/3337Y	
H8/3062	H8/336	
H8/3064	H8/337	
H8/3065	H8/338	
H8/3066	H8/3394	
H8/3067	H8/3396	
H8/3101	H8/3397	
H8/3102	H8/3434	
H8/3202	H8/3434F	
H8/3212	H8/3436	
H8/3214	H8/3437	
H8/3216	H8/350	
H8/3217	H8/3640	
H8/322	H8/3641	
H8/323	H8/3642	
H8/324	H8/3643	
H8/325	H8/3644	
H8/3256	H8/3812	
H8/3257	H8/3813	
H8/326	H8/3833	
H8/327	H8/3834	
H8/328	H8/3835	
H8/329	H8/3836	
H8/3292	H8/3837	
H8/3294	H8/520	
H8/3296	H8/532	
H8/3297	H8/534R	
H8/330	H8/534S	

- Support for 300, 300H and 500 family
- Support for 5V and 3.3V
- Full bondout support
- Up to 20MHz support
- On chip ROM and FLASH emulation
- Banking up to 16MByte (256 banks)
- Dual ported memory
- Software compatible ROM Monitor
- Interface with all compilers
- RTOS support
- CASE tool interface
- Windows9x, NT and X windows interface

TRACE32-ICEH8 supports most members of the H8 family from Renesas. The modular and open technology of the system allows the fast integration of new chip designs.

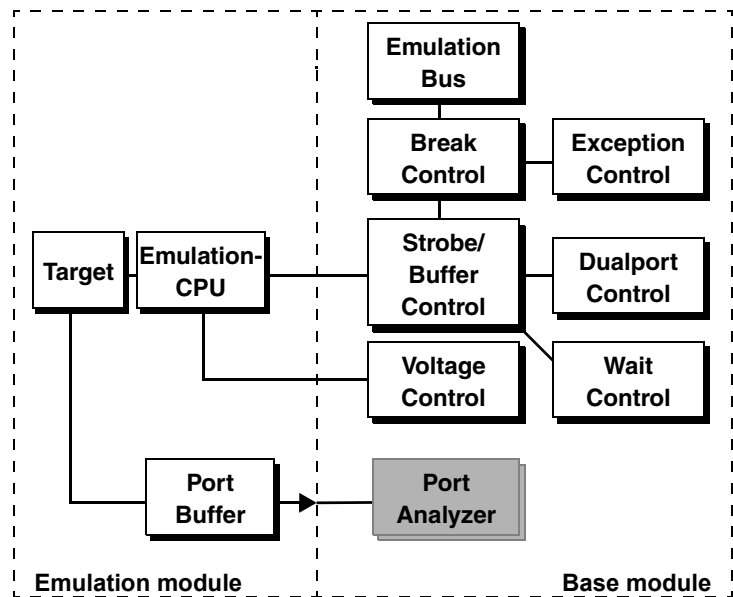
TRACE32-ICE is a state of the art In-Circuit Emulator, which offers unlimited hardware breakpoints and up to 16MByte dual-ported emulation memory. The real-time trace and trigger work up to the max. speed of the CPU. The analyzer offers selective trace as well as performance analysis and statistic functions.

The system offers an interface to all C and C++ compilers. Full RTOS support is available for HIOS/Renesas and Nucleus PLUS from Accelerated Technology.

TRACE32 works with the highest variety of host interfaces. The communication link to the host is done by the printer port, a fibre optic interface or ethernet allowing a high speed transfer.

Features

Basics of Operation



The ICE H8 probe is a high-performance emulation system for many derivatives of the H8 family. The change between different CPU types is done by changing the emulation module.

On the emulation base there is an extra slot for the TRACE32 Port Analyzer which can trace up to 64 port lines.

Operating Modes

The Emulator can work in stand-alone mode with internal clock or in active mode with internal or the target clock. On power-down of the target system the emulator tristates its output buffers and isolates its internal emulation circuits.

The operation modes are as follows:

- Reset Down
- Reset Up
- No Probe
- Alone Internal
- Alone External
- Emulation Internal
- Emulation External

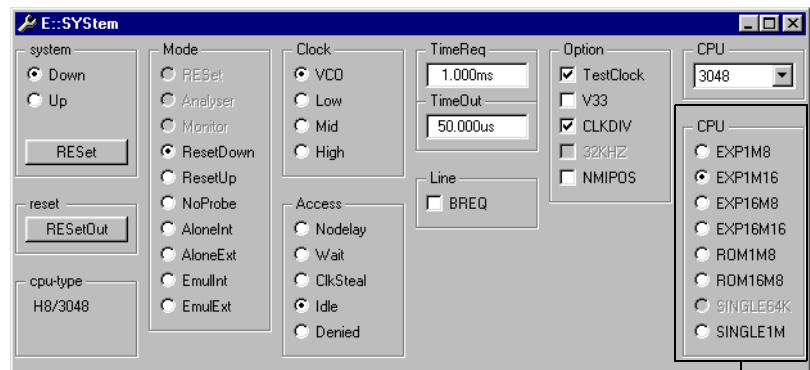
Clock

- Operation with external or internal clock

- Max. operation frequency:
20 MHz

Clock Fail Detection

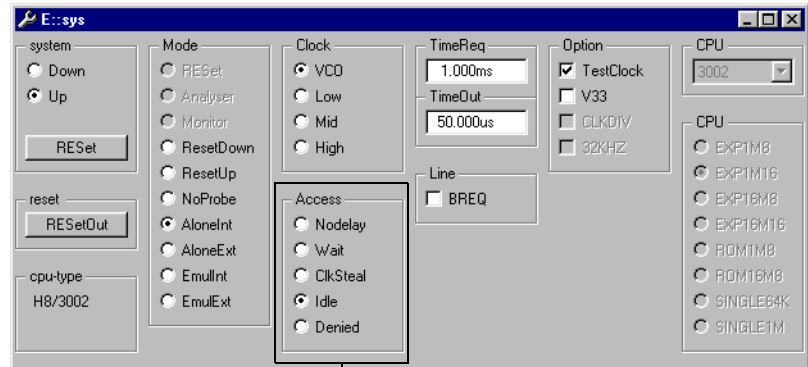
Support for all Operation Modes



Operation modes

ICE-H8 supports the realtime emulation of all operations modes at max. speed.

Dual-port Access



Dual port access modes

All TRACE32 memories are dual-ported. The dual-port access makes it possible to display and modify the contents of the overlay memory, to set or delete breakpoints or use the flag memory while the application is running in real-time.

The H8 has four dual-port modes:

- Nodelay
- Wait
- ClkSteal
- Idle

In **Nodelay** mode, the dual-port access is inserted in the regular bus cycles, generated by the CPU. This means that the CPU runs in real time during the dual-port access.

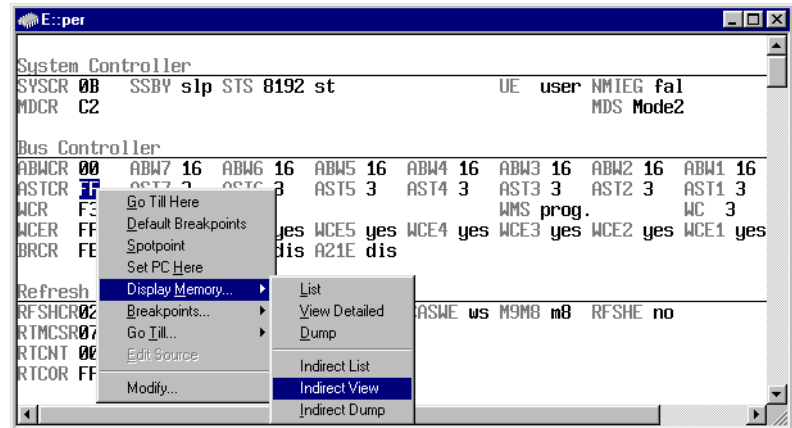
In **Wait** mode, wait states are inserted for the dual-port access via the wait pin. This is only possible, if the corresponding pins is configured as the wait pin.

In **ClkSteal** mode, the CPU clock is stopped for some cycles during the dual-port access. This works only if the emulator uses the internal VCO clock.

In **Idle** mode, the emulator waits on idle cycles of the CPU to perform the dual-port access. This cycles are generated for example during subroutine jumps.

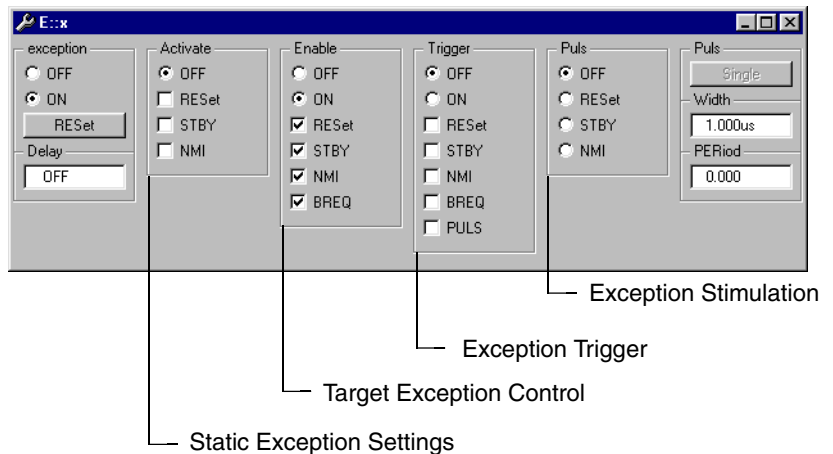
In the **Denied Mode** the dual-port access is switched off.

Peripheral Information



- Display of onchip peripherals
- User definable display of the onchip peripherals
- Definition is done interactive supported by softkeys
- Pull down menus for settings
- Additional description for each field
-

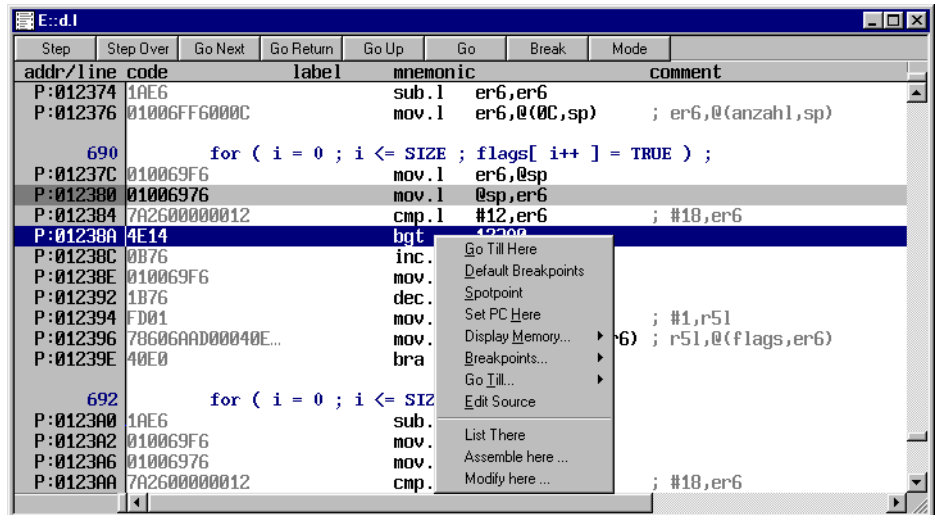
Exception Control



The TRACE32 exception controller allows to permanently activate an exception, to enable or disable specific

exceptions, to trigger on specific exceptions or to stimulate an exception.

HLL Debugging



Full support in real-time for:

- Break-before-line operation
- HLL single step in real-time

Trigger and trace on local variables

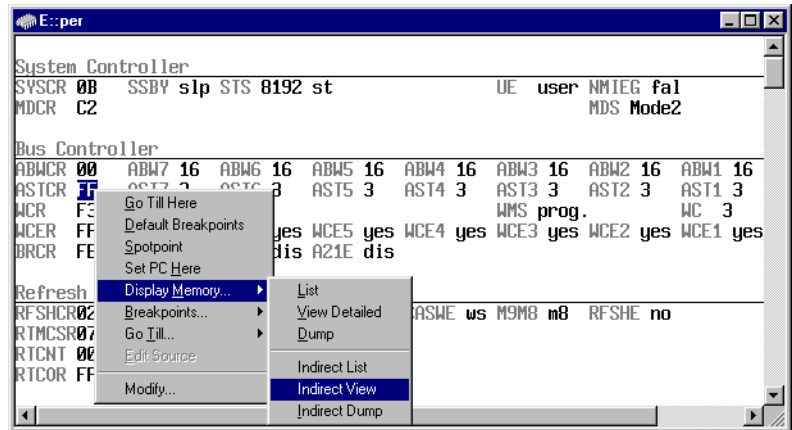
Background Task

The background task allows:

- To process interrupts

To maintain the refresh of a target DRAM while the emulation is stopped.

Peripheral Window



- Display of onchip peripherals
- User definable display of the onchip peripherals
- Definition is done interactive supported by softkeys
- Pull down menus for settings
- Additional description for each field
-

Memory Banking (H8/300 only)

ICE-H8 supports up to 256 banks for internal or external banking:

- Support for paged EPROMS
- Support for external banking using a CPU register or a CPU output pin. For external banking a separate banking probe is needed.

Emulation Modules

Modules Overview

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Debug Interfaces

TRACE32-PowerView supports most compilers, realtime operation systems and debuggers.

New integrations are mostly done on customers request. If your compiler or RTOS is not supported now, please ask us !

Compiler H8300

CPU	Language	Compier	Compan y	Option
H8/300	C	GNU-C	Free Software Foundati on, Inc.	COFF
H8/300	C	ICCH8300	IAR Systems AB	UBROF
H8/300	C	MCCH83	Mentor Graphics Corporati on	IEEE
H8/300	C	CH38	Renesas Technolo gy, Corp.	SYSROF
H8/300	C++	GNU-C++	Free Software Foundati on, Inc.	COFF

Compiler H8/300H

CPU	Language	Compiler	Company	Option
H8/300H	C	GNU-C	Free Software Foundation, Inc.	COFF
H8/300H	C	IARH8	IAR Systems AB	UBROF
H8/300H	C	CH38	Renesas Technology, Corp.	SYSROF
H8/300H	C++	GNU-C++	Free Software Foundation, Inc.	COFF

Compiler H8500

CPU	Language	Compiler	Company	Option
H8/500	C	ICCH8500	IAR Systems AB	UBROF
H8/500	C	MCCH85	Mentor Graphics Corporation	IEEE
H8/500	C	HICROSS-H8/500	NXP Semiconductors	HICROSS

RTOS Support

Company	Product	Comment
CMX Systems Inc.	CMX-RTX	
Renesas Technology, Corp.	HIOS	(HI8-3H), not supported in EUR
Mentor Graphics Corporation	Nucleus	
-	OSEK	via ORTI
Elektrobit Automotive GmbH	ProOSEK	via ORTI

Debugger Support

CPU	Debugger	Company
	WINDOWS CE PLATF. BUILDER	-
	CODE::BLOCKS	-
	C++TEST	-
	ADENEO	-
	X-TOOLS / X32	blue river software GmbH
	CODEWRIGHT	Borland Software Corporation
	CODE CONFIDENCE TOOLS	Code Confidence Ltd
	CODE CONFIDENCE TOOLS	Code Confidence Ltd
	EASYCODE	EASYCODE GmbH
	ECLIPSE	Eclipse Foundation, Inc

CPU	Debugger	Company
	CHRONVIEW	Inchron GmbH
	LDRA TOOL SUITE	LDRA Technology, Inc.
	UML DEBUGGER	LieberLieber Software GmbH
	SIMULINK	The MathWorks Inc.
	ATTOL TOOLS	MicroMax Inc.
	VISUAL BASIC INTERFACE	Microsoft Corporation
	LABVIEW	NATIONAL INSTRUMENTS Corporation
	RAPITIME	Rapita Systems Ltd.
	RHAPSODY IN MICROC	IBM Corp.
	RHAPSODY IN C++	IBM Corp.
	DA-C	RistanCASE
	TRACEANALYZER	Symtavision GmbH
	TA INSPECTOR	Timing Architects GmbH
	UNDODB	Undo Software
	VECTORCAST UNIT TESTING	Vector Software
	VECTORCAST CODE COVERAGE	Vector Software

Operation Frequency

The maximum operation frequency of TRACE32-ICEH8 depends on:

- The max. frequency of the CPU
- The access time of the overlay memory (15ns or 35ns)
- The mapper mode (**S**low or **F**ast)
- The number of waitstates
(W0 = 0 waitstates
W1 = 1 waitstate)
- The dual-port access mode

Idle Access

Dimensions

Module Dimensions

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Connectors

On each emulation module there are half-size connectors to:

- ❑ Connect the emulation module directly to the target by providing the corresponding connectors also on the target hardware

- ❑ Connect a standard adapter from Emulation Technologie, YAMAICHI, AMP etc.

The following table lists the physical dimensions of these half size connectors.
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Adapter

The adapters connect in different ways

- With Clip-Over Adapters the CPU can stay on the target board.
- With Solder-ON adapters the CPU must be removed

- YAMAICHI and AMP adapters fit to the CPU socket

The following table lists the physical dimensions of these adapters.

Not necessary.

Available Tool Chain

TRACE32 provides a complete set of development tools for the H8 family. This includes:

- The In-Circuit Emulator TRACE32-ICE
- The ROM Monitor based In-Circuit Debugger TRACE32-ICD

- Evaluation boards, which can be used until the target hardware is available.

The following list give an overview which development tools are available for the specific derivatives of the H8-family.
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Order Information

Module Description

Detailed Order Information

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