

In-Circuit Emulator for Freescale 68360/349

- 33 MHz zero-waitstate
- On-circuit emulation mode
- DRAM support
- Dual-ported memory
- Fast BDM interface
- Dynamic data trigger
- Interface with all Compilers
- C++ support
- CASE Tools Interface
- RTOS Support
- Windows9x, NT and Motif Interface

MC68349

MC68360

MC68EN360

MC68MH360

The ICE-68360 module support all CPU32+ derivatives up to 33 MHz at 3.3V or 5V. The modular and open technology of the system allows the fast integration of new chip designs.

A hardware accelerator for the BDM allows fast download to target memory. The module includes a dynamic data trigger system, which generates valid trigger signals on multicycle data accesses.

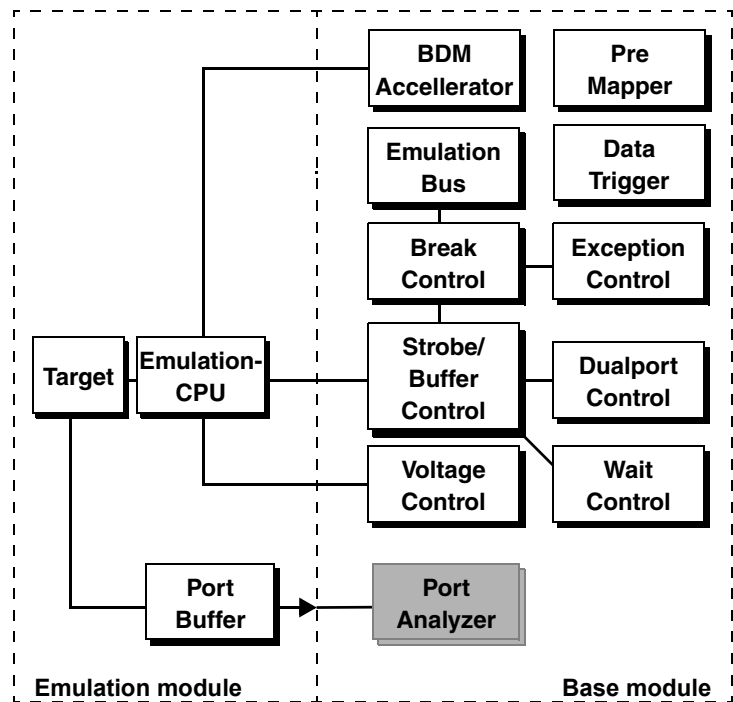
A software compatible BDM debugger is available.

TRACE32-ICE is a state of the art In-Circuit Emulator, which offers unlimited hardware breakpoints and up to 16MByte dual-ported emulation memory. The real-time trace and trigger work up to the max. speed of the CPU. The analyzer offers selective trace as well as performance analysis and statistic functions.

TRACE32 works with the highest variety of host interfaces. The communication link to the host is done by the printer port, a fibre optic interface or ethernet allowing a high-speed transfer.

Features

Basics of Operation



The ICE-68360 is a high performance emulation system for all CPU32+ derivatives from Motorola.

On the base module there is an extra slot for the TRACE32 Port Analyzer. Depending on the CPU type various signals can be traced.

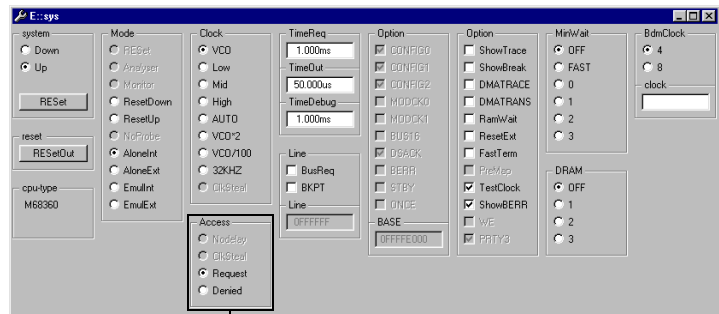
Operating Modes

The Emulator can work in stand-alone mode with internal clock or in active mode with internal or the target clock. On power-down of the target system the emulator tristates its output buffers and isolates its internal emulation circuits.

The operation modes are as follows:

- Reset Down
- Reset Up
- Alone Internal
- Alone External
- Emulation Internal
- Emulation External

Dual-Port Access



Dual port access mode

All TRACE32 memories are dual-ported. The dual-port access makes it possible to display and modify the contents of the overlay memory, to set or delete breakpoints or use the flag memory while the application is running in real-time.

The following dual-port access mode is implemented:

- Request

In the **Request mode** the dual port access is always possible.

In the **Denied mode** the dual port access is switched off.

Clock System

- Operation with external or internal clock
- Internal clock (EXTAL) 2 to 70 MHz

Internal Clocks

- VCO
- 5/10/20 MHz (EXTAL)
- 32 KHz
- VCO/100
- VCO*2

Wait System

- Additional wait cycles (1-15) can be specified
- Up to 250 wait cycles (4K blocks global and bitwise)

DMA Modes

DMA cycles are possible even if the realtime emulation is stopped.

- Trace and trigger also on DMA cycles
- Transparent

Full 32 Bit Address Mapping with Premapper

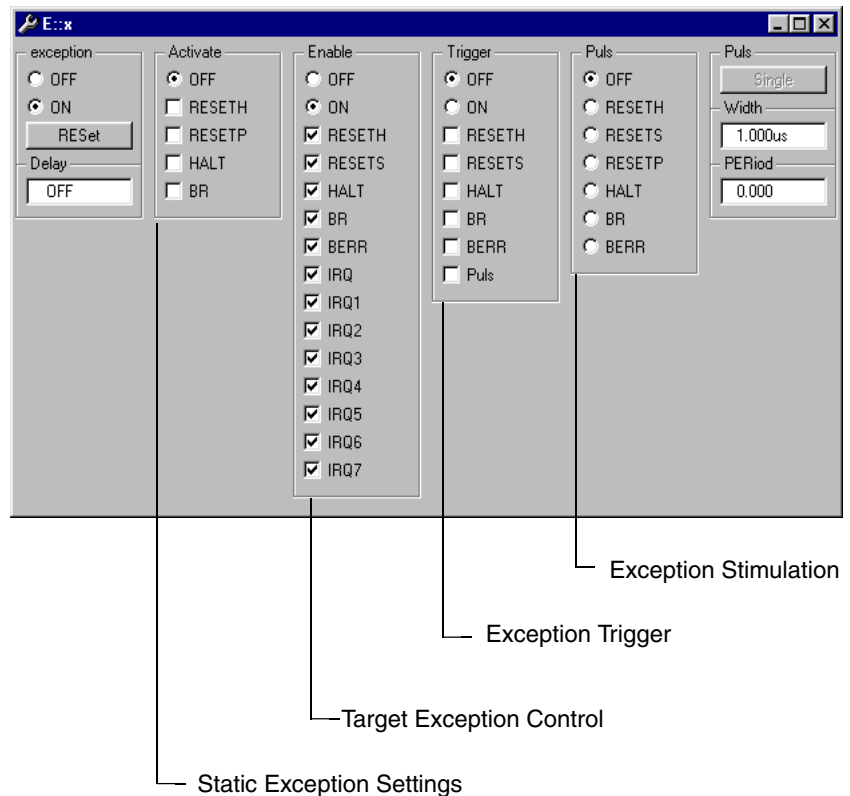
- 32 bit
 24 bit
 28 bit

Voltage and Clock Monitors for the Target System

Emulation System uses BDM Interface

- Hardware based protocol generator for max. debug speed
 10 MHz max. transfer speed

Exception Control



The TRACE32 exception controller allows you to permanently activate an exception, to enable or disable specific

exceptions, to trigger on specific exceptions or to stimulate an exception.

HLL-Debugger

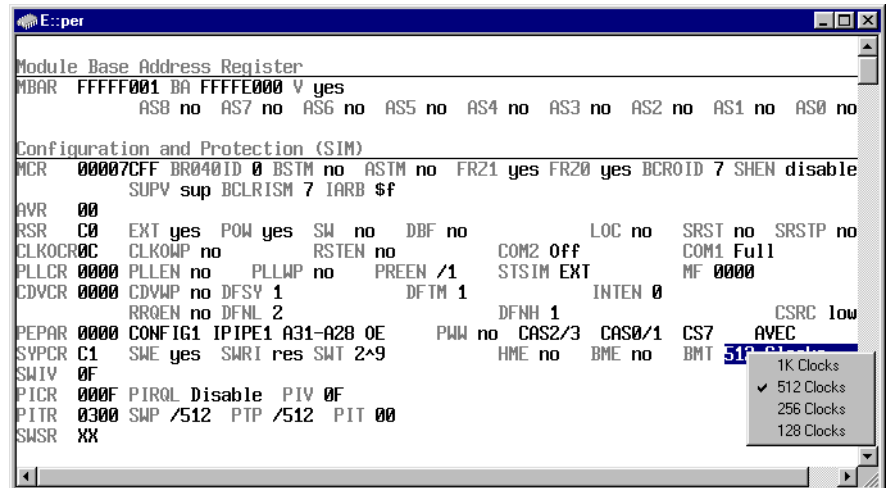
Step	Step Over	Go Next	Go Return	Go Up	Go	Break	Mode	
addr/line	code	label	mnemonic	comment				
	int sieve()			/* sieve of erathostenes */				
555	{							
SP:00001788	48E73830	sieve:	movem.l	[d2-d4,a2-a3],-(a7)				
SP:0000178C	227C00002774		movea.l	#2774,a1	; #flags,a1			
	register int i, prime, k;							
	int count;							
559	count = 0;							
SP:00001792	7200		moveq	#0,d1	; #0,count			
561	for (i = 0 ; i <= SIZE ; i++)							
SP:00001794	7400		moveq	#0,d1				
SP:00001796	2042		movea.l	d0				
SP:00001798	5282		addq.l	#1,d0				
SP:0000179A	13BC00018800		move.b	(a1,a0.l*1)				
SP:000017A0	7012		moveq	d0				
SP:000017A2	B082		cmp.l	d0,d0				

The TRACE32 debugger provides an interface to C, C++, ADA, MODULA, PASCAL and Pearl for all standard compilers. For more information on the supported compilers refer to the section "Debug Interfaces".

Full support in real-time for:

- ROM and external busses
- Break-before-line operation
- HLL single step in real-time
- Trigger and trace on local variables
- Trigger on bit variables

Peripheral Window



- Display of onchip peripherals
- User definable display of the onchip peripherals
- Definition is done interactive supported by softkeys
- Pull down menus for settings
- Additional description for each field
-

Reset Configuration by Software

ICE-68360 support a software based reset configuration:

- CONFIG
- MODCK
- BW16

Dynamic Data Selector

- Easy triggering on multi cycle, not aligned data transfers

Support for On-Circuit Emulation

Support for clip-over adapter

Emulation Modules

Modules Overview

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Debug Interfaces

TRACE32-PowerView supports most compilers, realtime operation systems and debuggers.

New integrations are mostly done on customers request. If your compiler or RTOS is not supported now, please ask us !

Compiler Support 68360

CPU	Language	Compier	Compan y	Option
68K	ADA	ALSYS-ADA		IEEE
68K	ADA	TELESOFT-ADA	Telesoft	IEEE
68K	ASM	RTOS	IEP GmbH	SYM/LOC
68K	ASM	ASM68K	Mentor Graphics Corporation	IEEE
68K	ASM	VERSADOS-ASM	NXP Semicon ductors	VERSADOS
68K	ASM	OS-9-ASSEMBLER	Radisys Inc.	ROF
68K	ASM	AS68	TASKING	IEEE
68K	C	HP-64000-C		HP
68K	C	ORGANON	CAD-UL Electronics Service s GmbH	BOUND
68K	C	C68K	Cosmic Software	COSMIC
68K	C	GNU-C	Free Software Foundati on, Inc.	ELF/DWARF
68K	C	GNU-C	Free Software Foundati on, Inc.	COFF
68K	C	GNU-C	Free Software Foundati on, Inc.	ELF/DWARF

CPU	Language	Compiler	Company	Option
68K	C	GREEN-HILLS-C	Greenhills Software Inc.	COFF
68K	C	ICC68K	Introl Corporation	ICOFF
68K	C	MCC	Mentor Graphics Corporation	IEEE
68K	C	HT-68K	Microchip Technology Inc.	HITECH
68K	C	HICROSS-68K	NXP Semiconductors	HICROSS
68K	C	CC68K	NXP Semiconductors	COFF
68K	C	ULTRA-C	Radisys Inc.	ROF
68K	C	OS/9-C	Radisys Inc.	ROF
68K	C	CROSSCODE-C	SDSI	SDS
68K	C	SCC68K	Sierra	COFF
68K	C	SUN3-CC	Oracle Corporation	DBX
68K	C	ICC68K	TASKING	COFF
68K	C	ICC68K	TASKING	IEEE
68K	C	TT-68K	TASKING	IEEE
68K	C	TCC68K	TASKING	AOUT
68K	C	TEKTRONIX-C	Tektronix	COMFOR
68K	C	D-CC	Wind River Systems	IEEE
68K	C	D-CC	Wind River Systems	ELF/DWARF

CPU	Language	Compiler	Company	Option
68K	C++	ORGANON-C++	CAD-UL Electronic Services GmbH	BOUND
68K	C++	GNU-C++	Free Software Foundati on, Inc.	DBX
68K	C++	GNU-C++	Free Software Foundati on, Inc.	ELF/DWARF
68K	C++	CCC68K	Mentor Graphics Corporati on	IEEE
68K	C++	HICROSS- 68K	NXP Semicon ductors	HICROSS
68K	C++	CODEWAR RIOR	NXP Semicon ductors	ELF/DWARF
68K	C++	CROSSCO DE-C++	SDSI	SDS
68K	C++	D-C++	Wind River Systems	ELF/DWARF
68K	MODULA	MOD68K	Introl Corporati on	ICOFF
68K	MODULA	MCS2	Multicha nnelsyst ems GmbH	COFF
68K	MODULA	MCDS	NXP Semicon ductors	MCDS
68K	PASCAL	MPC	Mentor Graphics Corporati on	IEEE
68K	PEARL	RTOS	IEP GmbH	SYM/LOC

RTOS Support

Company	Product	Comment
Atego Ltd.	AdaWorld ARTK	
KadakProducts Ltd.	AMX	
Oracle Corporation	ChorusOS	
CMX Systems Inc.	CMX-RTX	
Synopsys, Inc	MQX	2.40 and 2.50, 3.6
	MTOS-UX	
Mentor Graphics Corporation	Nucleus PLUS	
Radisys Inc.	OS-9	
Enea OSE Systems	OSE Classic	(OS68)
Enea OSE Systems	OSE Delta	4.x and 5.x
	RealTime Craft	(XEC68k)
Quadros Systems Inc.	RTXC 3.2	
IBM Corp.	SDT-Cmicro	
-	uCLinux	Kernel Version 2.4 and 2.6, 3.x
Mentor Graphics Corporation	VRTX32	
Mentor Graphics Corporation	VRTXmc	
Mentor Graphics Corporation	VRTXsa	
Wind River Systems	VxWorks	5.x and 6.x

Debugger Support

CPU	Debugger	Company
	WINDOWS CE PLATF. BUILDER	-
	CODE::BLOC KS	-
	C++TEST	-
	ADENEO	-
	X-TOOLS / X32	blue river software GmbH
	CODEWRIGH T	Borland Software Corporation
	CODE CONFIDENC E TOOLS	Code Confidence Ltd
	CODE CONFIDENC E TOOLS	Code Confidence Ltd
	EASYCODE	EASYCODE GmbH
	ECLIPSE	Eclipse Foundation, Inc
	CHRONVIEW	Inchron GmbH
	LDRA TOOL SUITE	LDRA Technology, Inc.
	UML DEBUGGER	LieberLieber Software GmbH
	SIMULINK	The MathWorks Inc.
	ATTOL TOOLS	MicroMax Inc.
	VISUAL BASIC INTERFACE	Microsoft Corporation
	LABVIEW	NATIONAL INSTRUMENT S Corporation
	RAPITIME	Rapita Systems Ltd.
	RHAPSODY IN MICROC	IBM Corp.

CPU	Debugger	Company
	RHAPSODY IN C++	IBM Corp.
	DA-C	RistanCASE
	TRACEANALY ZER	Symtavisio n GmbH
	TA INSPECTOR	Timing Architects GmbH
	UNDODB	Undo Software
	VECTORCAS T UNIT TESTING	Vector Software
	VECTORCAS T CODE COVERAGE	Vector Software
68K	OS68 DEBUGGER	Enea OSE Systems
68K	SDT CMICRO	IBM Corp.
68K	DIAB RTA SUITE	Wind River Systems

Operation Voltage and Frequency

The maximum operation frequency of TRACE32-ICE depends on:

- The max. frequency of the CPU
- The access time of the overlay memory (15ns or 35ns)
- The mapper mode (**Slow** or **Fast**)
- The number of waitstates (W0 = 0 waitstates, W1 = 1 waitstate)
- The dual-port access mode

Denied and Request Access

Operating Voltage

This list contains information on probes available for other voltage ranges.

Probes not noted here supply an operation voltage range from 4.5V to 5.5V. tbd

Dimensions

Module Dimensions

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Connectors

On each emulation module there are half-size connectors to:

- ❑ Connect the emulation module directly to the target by providing the corresponding connectors also on the target hardware

- ❑ Connect a standard adapter from Emulation Technology, YAMAICHI, AMP, TOKYO ELETECH etc.

The following table lists the physical dimensions of these connectors.

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Adapter

The adapters connect in different ways

- With Clip-Over Adapters the CPU can stay on the target board.
- With Solder-ON adapters the CPU must be removed

- YAMAICHI and AMP adapters fit to the CPU socket

The following table lists the physical dimensions of these adapters.

Not necessary.

Available Tool Chain

TRACE32 provides a complete set of development tools for this family. This includes:

- The In-Circuit Emulator TRACE32-ICE
- The high speed RISC Emulator TRACE32-FIRE
- The BDM/JTAG/ONCE etc. based In-Circuit Debugger TRACE32-ICD
- The ROM Monitor based In-Circuit Debugger TRACE32-ICD
- The ICD Trace, a trace extension to the BDM/JTAG debuggers or ROM monitors
- Evaluation boards, which can be used until the target hardware is available.
- The Instruction Set Simulator (SIM), a software tool for code test without any hardware

The following list give an overview which development tools are available for the specific derivatives of this family.

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Order Information

Module Description

Detailed Order Information

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