
Target Adaption for MPC8240

Signal	Ball Pin	Pin Ball	Signal
GND	1	2	GND
VDD	3	4	VDD
GVDD	5	6	GVDD
OVDD	7	8	OVDD
GND	9	10	GND
DL17	A1 11	12	B1 DL16
SDRAMSYNCOOUT	C1 13	14	C2 PAR7
SDRAMCLK0	D1 15	16	D2 PAR6
DH19	D4 17	18	E1 SDRAMCLK3
PAR3	E2 19	20	E4 DH16
MTP1	F1 21	22	F2 QACK-
SDRAMCLK1	G1 23	24	G2 SDRAMCLK2
PAR2	G4 25	26	H1 FOE-
CKE	H2 27	28	H3 SDRAMSYNCHIN
CAS7-	J1 29	30	J2 MTP0
CAS6-	K1 31	32	K2 CAS3-
CAS2-	K3 33	34	L1 CS7-
CS5-	L2 35	36	M1 CS6-
CS4-	M2 37	38	N1 SDMA1
RCS1-	N2 39	40	N4 RCS0-
SDMA0	P1 41	42	P2 SDBA0
SDMA2	R1 43	44	R2 SDMA3
SDMA4	T1 45	46	T2 SDMA5
SDMA8	U1 47	48	U2 SDMA7
SDMA6	U4 49	50	V1 SDMA9
SDMA10	V3 51	52	W1 SDMA11
SDMA12	W2 53	54	Y1 AS-
RTC	Y2 55	56	Y4 CS0-
WE-	AA1 57	58	AA3 CS1-
CS2-	AA4 59	60	AB1 CAS0-
CAS1-	AB2 61	62	AC1 CAS4-
CAS5-	AC2 63	64	AD1 SDRAS-
SDCAS-	AD2 65	66	AE1 MAA2
N/C	67	68	N/C
N/C	69	70	N/C
GND	71	72	GND
OVDD	73	74	OVDD
GVDD	75	76	GVDD
VDD	77	78	VDD
GND	79	80	GND

Signal	Ball Pin	Pin Ball	Signal
GND	81	82	GND
VDD	83	84	VDD
GVDD	85	86	GVDD
OVDD	87	88	OVDD
GND	89	90	GND
MAA1	AF1 91	92	AF2 MAA0
PAR0	AF3 93	94	AE3 PAR1
PAR5	AF4 95	96	AE4 PAR4
CS3-	AC4 97	98	AF5 DH14
DL15	AE5 99	100	AC5 DH15
DL14	AF6 101	102	AE6 DH13
DL13	AF7 103	104	AE7 DH12
DH11	AC7 105	106	AF8 DL12
DH10	AE8 107	108	AD8 DL11
DL10	AF9 109	110	AE9 DH09
DL09	AF10 111	112	AE10 DH08
DL08	AF11 113	114	AE11 DH07
DL07	AF12 115	116	AE12 DH06
DL06	AF13 117	118	AE13 DL05
DH05	AC13 119	120	AF14 DH04
DH03	AE14 121	122	AC14 DL04
DL03	AF15 123	124	AE15 DL02
DH01	AF16 125	126	AE16 DH02
FTP1	AF17 127	128	AE17 DL01
DL00	AD17 129	130	AC17 DH00
PMAA1	AF18 131	132	AD18 PMAA0
TRIGOUT	AC18 133	134	AF19 FTP0
PMAA2	AE19 135	136	AF20 TRIGIN
SDA	AE20 137	138	AF21 SCL
OSCIN	AD21 139	140	AC21 TDO
TCK	AF22 141	142	AE22 TMS
TEST-	AD22 143	144	AC22 IRQ2-
TDI	AF23 145	146	AE23 TRST-
AVDD2	AF24 147	148	AE24 IRQ3-
PCICLK3	AF25 149	150	N/C
GND	151	152	GND
OVDD	153	154	OVDD
GVDD	155	156	GVDD
VDD	157	158	VDD
GND	159	160	GND

Signal	Ball	Pin	Pin	Ball	Signal
GND		161		162	GND
VDD		163		164	VDD
GVDD		165		166	GVDD
OVDD		167		168	OVDD
GND		169		170	GND
PCICLK4	AF26	171		172	AE26 PCICLK2
FTP2	AD26	173		174	AD25 PCISYNCOU
INTA-	AC26	175		176	AC25 PCICLK0
REQ0-	AB26	177		178	AB25 PCICLK1
PCISYN	AB23	179		180	AA26 REQ2-
REQ1-	AA25	181		182	Y26 REQ4-
REQ3-	Y25	183		184	W26 GNT4-
GNT3-	W25	185		186	W24 GNT2-
GNT1-	W23	187		188	V26 GNT0-
AD31	V25	189		190	U26 AD29
AD30	U25	191		192	U24 AD28
AD27	U23	193		194	T26 AD25
AD26	T25	195		196	R26 AD23
AD24	R25	197		198	P26 IDSEL
BE3-	P25	199		200	N26 AD22
AD21	N25	201		202	N23 AD20
AD19	M26	203		204	M25 AD18
AD16	L26	205		206	L25 AD17
TRDY-	K26	207		208	K25 IRDY-
BE2-	K23	209		210	J26 LOCK-
FRAME-	J24	211		212	H26 DEVSEL-
STOP-	H25	213		214	G26 PERR-
PAR	G25	215		216	F26 SERR-
AD15	F24	217		218	F23 BE1-
AD14	E26	219		220	E25 AD13
AD12	E23	221		222	D26 AD11
AD10	D25	223		224	C26 AD09
FTP3	C25	225		226	B26 AD07
N/C		227		228	N/C
N/C		229		230	N/C
GND		231		232	GND
OVDD		233		234	OVDD
GVDD		235		236	GVDD
VDD		237		238	VDD
GND		239		240	GND

Signal	Ball	Pin	Pin	Ball	Signal	
GND		241		242	GND	
VDD		243		244	VDD	
GVDD		245		246	GVDD	
OVDD		247		248	OVDD	
GND		249		250	GND	
AD08	A26	251		252	A25	BE0-
AD06	A24	253		254	B24	AD05
IRQ4-	A23	255		256	B23	AD03
PLLCFG0	A22	257		258	B22	AD02
AD00	C22	259		260	D22	AD01
PLLCFG2	A21	261		262	B21	IRQ1-
HRSTCTRL-	A20	263		264	B20	SUSPEND-
HRSTCPU-	A19	265		266	B19	PLLCFG1
IRQ0-	C19	267		268	D19	AD04
SMI-	A18	269		270	B18	PLLCFG3
MCP-	A17	271		272	B17	PLLCFG4
AVDD	C17	273		274	D17	IAVDD
MIV-	A16	275		276	B16	SRESET-
NMI	D16	277		278	A15	DH31
CKO	B15	279		280	A14	DL31
TBEN	B14	281		282	D14	CHKSTOPIN-
DH29	A13	283		284	B13	DH30
DL28	A12	285		286	B12	DL30
DH28	A11	287		288	B11	DL29
DL26	A10	289		290	B10	DH27
DL27	D10	291		292	A9	DH26
DH25	C9	293		294	A8	DL24
DL25	B8	295		296	A7	DL22
DH24	B7	297		298	D7	DL23
DL21	A6	299		300	C6	DH23
DH22	D6	301		302	A5	DL20
DH21	B5	303		304	A4	DL19
DH20	B4	305		306	A3	DL18
DH18	B3	307		308	A2	DH17
N/C		309		310		N/C
GND		311		312		GND
OVDD		313		314		OVDD
GVDD		315		316		GVDD
VDD		317		318		VDD
GND		319		320		GND

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