



## RTOS Debugger for AMX

- Works with Intel, Microsoft, Borland and many other Compilers

### C++ support

- Up to 40 MHz no-waitstate emulation
- Dual ported memory
- FPU support
- Support for all 5V and 3.3V derivatives
- Banking support for 16 banks (186) or 256 banks (196)
- Supports memory split for program and data
- CASE tools interface
- RTOS support

The TRACE32-ICE186 supports microcontroller from AMD, NEC and Intel. Its modular and open technology allows the fast integration of new chip designs.

A software compatible ROM Monitor is also available.

TRACE32 works with the highest variety of host interfaces. The communication link to the host is done by the printer port, a fibre optic interface or ethernet allowing a high-speed transfer.

Body

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## Heading1

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### Heading2

- Bullet
- Bullet1
- Bulet2

HeadingRunIn.

The TRACE32 multitask debugger for AMX provides display functions for all AMX resources. The system summary, tasks, timers, mailboxes, message exchanges, semaphores, events and pools can be displayed. In addition, by using the emulators dual-port memory,

the display of all these regions can be viewed non-intrusively in real time. The tables are updated permanently ("On The Fly"), without affecting the application at all.

which task was active at a specific time, giving a clear view of the behaviour of the system.

ferent tasks call one single function at the same time, or if a task switch occurs in between the function.

#### **RepsHead**

RepsBody

**RespSep**